## **TPC Benchmark™ C**

## **Full Disclosure Report**



First Edition 31–Aug–2022

Using

**Goldilocks v3.1 Standard Edition** 

on

Supermicro A+ Server 1124US-TNRP

#### First Edition: 31-Aug-2022

TTA, Telecommunications Technology Association, believes that all the information in this document is accurate as of the publication date. The information in this document is subject to change without notice. TTA, the sponsor of this benchmark test, assumes no responsibility for any errors that may appear in this document. The pricing information in this document is believed to accurately reflect the current prices as of the publication date. However, the sponsor provides no warranty of the pricing information in this document.

Benchmark results are highly dependent upon workload, specific application requirements, and system design and implementation. Relative system performance will vary as a result of these and other factors. Therefore, the TPC Benchmark™ C should not be used as a substitute for a specific customer application benchmark when critical capacity planning and/or product evaluation decisions are contemplated.

All performance data contained in this report was obtained in a rigorously controlled environment. Results obtained in other operating environments may vary significantly. No warranty of system performance or price/performance is expressed or implied in this report.

#### Trademarks

The following terms used in this publication are trademarks of other companies as follows:

- TPC Benchmark, TPC-C, and tpmC are trademarks of the Transaction Processing Performance Council
- TTA is a registered trademark of Telecommunications Technology Association
- Goldilocks is a registered trademark of SUNJESOFT, Inc.
- JBoss is a registered trademark of RedHat, Inc.
- AMD and EPYC are trademarks of AMD Corporation..
- All other trademarks and copyrights are properties of their respective owners.

# **Table of Contents**

TABLE OF CONTENTS	
ABSTRACT	5
PREFACE	6
GENERAL ITEMS	
0.1 APPLICATION CODE AND DEFINITION STATEMENTS 0.2 BENCHMARK SPONSOR 0.3 PARAMETER SETTINGS 0.4 CONFIGURATION DIAGRAMS	
CLAUSE 1: LOGICAL DATABASE DESIGN	
1.1 TABLE DEFINITIONS 1.2 PHYSICAL ORGANIZATION OF DATABASE 1.3 INSERT AND DELETE OPERATIONS 1.4 HORIZONTAL OR VERTICAL PARTITIONING 1.5 REPLICATION OR DUPLICATION	
CLAUSE 2: TRANSACTION AND TERMINAL PROFILES	
<ul> <li>2.1 RANDOM NUMBER GENERATION</li> <li>2.2 INPUT/OUTPUT SCREENS</li> <li>2.3 PRICED TERMINAL FEATURE</li> <li>2.4 PRESENTATION MANAGERS</li> <li>2.5 TRANSACTION STATISTICS</li> <li>2.6 QUEUING MECHANISM</li> </ul>	
CLAUSE 3: TRANSACTION AND SYSTEM PROPERTIES	
<ul> <li>3.1 ATOMICITY</li></ul>	16 16 16 16 16 16 17 21 21
CLAUSE 4: SCALING AND DATABASE POPULATION	
<ul> <li>4.1 CARDINALITY OF TABLES</li></ul>	
CLAUSE 5: PERFORMANCE METRICS	
<ul> <li>5.1 TPC BENCHMARK C METRICS</li></ul>	

5.4.2 Response Time versus throughput	5.4.1 Response Time frequency distribution curves	
5.4.3 Think Time frequency distribution       3         5.4.4 Throughput versus elapsed time       3         5.5 STEADY STATE DETERMINATION       3         5.6 WORK PERFORMED DURING STEADY STATE       3         5.7 MEASUREMENT PERIOD DURING STEADY STATE       3         5.7 MEASUREMENT PERIOD DURING STEADY STATE       3         5.7 MEASUREMENT PERIOD DURATION       3         5.8 TRANSACTION STATISTICS       3         5.9 CHECKPOINTS       3         CLAUSE 6: SUT, DRIVER AND COMMUNICATION       3         6.1 REMOTE TERMINAL EMULATOR (RTE)       3         6.2 EMULATED COMPONENTS       3         6.3 FUNCTIONAL DIAGRAMS       3         6.4 NETWORKS       3         6.5 OPERATOR INTERVENTION       3         7.1 HARDWARE AND SOFTWARE PRICING       3         7.2 THRE YEAR PRICE       3         7.3 AVAILABILITY DATES       3         7.4 VALUES 8: REPORTING       3         8.1 FULL DISCLOSURE REPORT       3         9.1 AUDITOR INFORMATION       3         9.1 AUDITOR INFORMATION       3         9.2 ATTESTATION LETTER       3         34       35         9.1 AUDITOR INFORMATION       3         9.2 ATTESTATION LETTER       3 <th></th> <th></th>		
5.4.4 Throughput versus elapsed time       32         5.5 STEADY STATE DETERMINATION       33         5.6 WORK PERFORMED DURING STEADY STATE       33         5.7 MEASUREMENT PERIOD DURATION       32         5.8 TRANSACTION STATISTICS       33         5.9 CHECKPOINTS       33         CLAUSE 6: SUT, DRIVER AND COMMUNICATION       36         6.1 REMOTE TERMINAL EMULATOR (RTE)       36         6.2 EMULATED COMPONENTS       36         6.3 FUNCTIONAL DIAGRAMS       36         6.4 NETWORKS       36         6.5 OPERATOR INTERVENTION       36         CLAUSE 7: PRICING       37         7.1 HARDWARE AND SOFTWARE PRICING       37         7.2 THREE YEAR PRICE       37         7.3 AVAILABILITY DATES       37         7.4 AUSE 8: REPORTING       38         8.1 FULL DISCLOSURE REPORT       38         8.1 FULL DISCLOSURE REPORT       36         9.1 AUDITOR INFORMATION       36         9.1 AUDITOR INFORMATION       36         9.2 ATTESTATION LETTER       35         9.2 ATTESTATION LETTER       36         9.1 AUDITOR INFORMATION       36         9.2 ATTESTATION LETTER       36         9.2 ATTESTATION LETTER       36		
5.6 WORK PERFORMED DURING STEADY STATE35.7 MEASUREMENT PERIOD DURATION35.8 TRANSACTION STATISTICS35.9 CHECKPOINTS3CLAUSE 6: SUT, DRIVER AND COMMUNICATION36.1 REMOTE TERMINAL EMULATOR (RTE)36.2 EMULATED COMPONENTS36.3 FUNCTIONAL DIAGRAMS36.4 NETWORKS36.5 OPERATOR INTERVENTION37.1 HARDWARE AND SOFTWARE PRICING37.2 THREE YEAR PRICE37.3 AVAILABILITY DATES37.3 AVAILABILITY DATES38.1 FULL DISCLOSURE REPORT39.1 AUDITOR INFORMATION39.1 AUDITOR INFORMATION39.2 ATTESTATION LETTER3APPENDIX A: SOURCE CODE4APPENDIX B: TUNABLE PARAMETERS4		
5.7 MEASUREMENT PERIOD DURATION325.8 TRANSACTION STATISTICS325.9 CHECKPOINTS32CLAUSE 6: SUT, DRIVER AND COMMUNICATION366.1 REMOTE TERMINAL EMULATOR (RTE)366.2 EMULATED COMPONENTS366.3 FUNCTIONAL DIAGRAMS366.4 NETWORKS366.5 OPERATOR INTERVENTION36CLAUSE 7: PRICING377.1 HARDWARE AND SOFTWARE PRICING377.2 THRE YEAR PRICE377.3 AVAILABILITY DATES368.1 FULL DISCLOSURE REPORT388.1 FULL DISCLOSURE REPORT369.1 AUDITOR INFORMATION399.2 ATTESTATION LETTER36APPENDIX A: SOURCE CODE44APPENDIX B: TUNABLE PARAMETERS44		
5.8 TRANSACTION STATISTICS       32         5.9 CHECKPOINTS       32         CLAUSE 6: SUT, DRIVER AND COMMUNICATION       36         6.1 REMOTE TERMINAL EMULATOR (RTE)       36         6.2 EMULATED COMPONENTS       36         6.3 FUNCTIONAL DIAGRAMS       36         6.4 NETWORKS       36         6.5 OPERATOR INTERVENTION       36         CLAUSE 7: PRICING       37         7.1 HARDWARE AND SOFTWARE PRICING       37         7.2 THREE YEAR PRICE       37         7.3 AVAILABILITY DATES       36         8.1 FULL DISCLOSURE REPORT       38         8.1 FULL DISCLOSURE REPORT       36         9.1 AUDITOR INFORMATION       36         9.2 ATTESTATION LETTER       37         APPENDIX A: SOURCE CODE       42         APPENDIX B: TUNABLE PARAMETERS       44		
5.9 CHECKPOINTS33CLAUSE 6: SUT, DRIVER AND COMMUNICATION366.1 REMOTE TERMINAL EMULATOR (RTE)366.2 EMULATED COMPONENTS366.3 FUNCTIONAL DIAGRAMS366.4 NETWORKS366.5 OPERATOR INTERVENTION367.0 FURCING377.1 HARDWARE AND SOFTWARE PRICING377.2 THREE YEAR PRICE377.3 AVAILABILITY DATES377.3 AVAILABILITY DATES378.1 FULL DISCLOSURE REPORT388.1 FULL DISCLOSURE REPORT369.1 AUDITOR INFORMATION359.2 ATTESTATION LETTER36APPENDIX A: SOURCE CODE42APPENDIX B: TUNABLE PARAMETERS44		
CLAUSE 6: SUT, DRIVER AND COMMUNICATION306.1 REMOTE TERMINAL EMULATOR (RTE)366.2 EMULATED COMPONENTS336.3 FUNCTIONAL DIAGRAMS366.4 NETWORKS366.5 OPERATOR INTERVENTION36CLAUSE 7: PRICING377.1 HARDWARE AND SOFTWARE PRICING377.2 THREE YEAR PRICE377.3 AVAILABILITY DATES37CLAUSE 8: REPORTING388.1 FULL DISCLOSURE REPORT389.1 AUDITOR ATTESTATION399.2 ATTESTATION LETTER39APPENDIX A: SOURCE CODE44APPENDIX B: TUNABLE PARAMETERS44		
6.1 REMOTE TERMINAL EMULATOR (RTE)366.2 EMULATED COMPONENTS366.3 FUNCTIONAL DIAGRAMS366.4 NETWORKS366.5 OPERATOR INTERVENTION36CLAUSE 7: PRICING377.1 HARDWARE AND SOFTWARE PRICING377.2 THREE YEAR PRICE377.3 AVAILABILITY DATES37CLAUSE 8: REPORTING368.1 FULL DISCLOSURE REPORT369.1 AUDITOR ATTESTATION399.1 AUDITOR INFORMATION399.2 ATTESTATION LETTER32APPENDIX A: SOURCE CODE44APPENDIX B: TUNABLE PARAMETERS44	5.9 CHECKPOINTS	
6.2 EMULATED COMPONENTS366.3 FUNCTIONAL DIAGRAMS366.4 NETWORKS366.4 NETWORKS366.5 OPERATOR INTERVENTION36CLAUSE 7: PRICING377.1 HARDWARE AND SOFTWARE PRICING377.2 THREE YEAR PRICE377.3 AVAILABILITY DATES37CLAUSE 8: REPORTING368.1 FULL DISCLOSURE REPORT369.1 AUDITOR ATTESTATION399.1 AUDITOR INFORMATION399.2 ATTESTATION LETTER35APPENDIX A: SOURCE CODE44APPENDIX B: TUNABLE PARAMETERS44	CLAUSE 6: SUT, DRIVER AND COMMUNICATION	
6.3 FUNCTIONAL DIAGRAMS.366.4 NETWORKS366.5 OPERATOR INTERVENTION36 <b>CLAUSE 7: PRICING.</b> 377.1 HARDWARE AND SOFTWARE PRICING377.2 THREE YEAR PRICE377.3 AVAILABILITY DATES37 <b>CLAUSE 8: REPORTING.</b> 388.1 FULL DISCLOSURE REPORT388.1 FULL DISCLOSURE REPORT369.1 AUDITOR ATTESTATION.399.1 AUDITOR INFORMATION.399.2 ATTESTATION LETTER36APPENDIX A: SOURCE CODE42APPENDIX B: TUNABLE PARAMETERS44		
6.4 NETWORKS366.5 OPERATOR INTERVENTION36CLAUSE 7: PRICING377.1 HARDWARE AND SOFTWARE PRICING377.2 THREE YEAR PRICE377.3 AVAILABILITY DATES37CLAUSE 8: REPORTING388.1 Full Disclosure Report388.1 Full Disclosure Report389.1 Auditor Information399.1 Auditor Information399.2 ATTESTATION LETTER32APPENDIX A: SOURCE CODE42APPENDIX B: TUNABLE PARAMETERS44		
6.5 OPERATOR INTERVENTION36CLAUSE 7: PRICING377.1 HARDWARE AND SOFTWARE PRICING377.2 THREE YEAR PRICE377.3 AVAILABILITY DATES37CLAUSE 8: REPORTING388.1 Full Disclosure Report38CLAUSE 9: AUDITOR ATTESTATION399.1 AUDITOR INFORMATION399.2 ATTESTATION LETTER39APPENDIX A: SOURCE CODE42APPENDIX B: TUNABLE PARAMETERS44		
CLAUSE 7: PRICING.3'7.1 HARDWARE AND SOFTWARE PRICING3'7.2 THREE YEAR PRICE3'7.3 AVAILABILITY DATES3'CLAUSE 8: REPORTING.3'8.1 FULL DISCLOSURE REPORT3'CLAUSE 9: AUDITOR ATTESTATION.3'9.1 AUDITOR INFORMATION.3'9.2 ATTESTATION LETTER3'APPENDIX A: SOURCE CODE4'APPENDIX B: TUNABLE PARAMETERS4'		
7.1 HARDWARE AND SOFTWARE PRICING3'7.2 THREE YEAR PRICE3'7.3 AVAILABILITY DATES3'CLAUSE 8: REPORTING3'8.1 Full Disclosure Report3'CLAUSE 9: AUDITOR ATTESTATION3'9.1 Auditor Information3'9.2 ATTESTATION LETTER3'APPENDIX A: SOURCE CODE4'APPENDIX B: TUNABLE PARAMETERS4'	6.5 OPERATOR INTERVENTION	
7.2 THREE YEAR PRICE377.3 AVAILABILITY DATES37 <b>CLAUSE 8: REPORTING</b> 388.1 FULL DISCLOSURE REPORT38 <b>CLAUSE 9: AUDITOR ATTESTATION</b> 399.1 AUDITOR INFORMATION399.2 ATTESTATION LETTER35 <b>APPENDIX A: SOURCE CODE</b> 42 <b>APPENDIX B: TUNABLE PARAMETERS</b> 44	CLAUSE 7: PRICING	
7.3 AVAILABILITY DATES37CLAUSE 8: REPORTING388.1 FULL DISCLOSURE REPORT38CLAUSE 9: AUDITOR ATTESTATION399.1 AUDITOR INFORMATION399.2 ATTESTATION LETTER39APPENDIX A: SOURCE CODE42APPENDIX B: TUNABLE PARAMETERS44	7.1 HARDWARE AND SOFTWARE PRICING	
CLAUSE 8: REPORTING.388.1 Full Disclosure Report36CLAUSE 9: AUDITOR ATTESTATION.399.1 Auditor Information.399.2 ATTESTATION LETTER39APPENDIX A: SOURCE CODE42APPENDIX B: TUNABLE PARAMETERS44		
8.1 Full Disclosure Report       38         CLAUSE 9: AUDITOR ATTESTATION.       39         9.1 Auditor Information.       39         9.2 Attestation Letter       39         APPENDIX A: SOURCE CODE       42         APPENDIX B: TUNABLE PARAMETERS       44	7.3 AVAILABILITY DATES	
CLAUSE 9: AUDITOR ATTESTATION	CLAUSE 8: REPORTING	
9.1 Auditor Information	8.1 FULL DISCLOSURE REPORT	
9.2 ATTESTATION LETTER       39         APPENDIX A: SOURCE CODE       42         APPENDIX B: TUNABLE PARAMETERS       44	CLAUSE 9: AUDITOR ATTESTATION	
APPENDIX A: SOURCE CODE	9.1 AUDITOR INFORMATION	
APPENDIX B: TUNABLE PARAMETERS 44	9.2 ATTESTATION LETTER	
	APPENDIX A: SOURCE CODE	
APPENDIX C: PRICE QUOTATIONS	APPENDIX B: TUNABLE PARAMETERS	
	APPENDIX C: PRICE QUOTATIONS	

## Abstract

This report documents the methodology and results of the TPC Benchmark <sup>™</sup> C (TPC-C) test conducted by TTA on the Goldilocks v3.1 Standard Edition on Supermicro A+ Server 1124US-TNRP

Goldilocks v3.1 Standard Edition on Supermicro A+ Server 1124US-TNRP

Company Name	System Name	Database Software	Operating System
Telecommunications	Supermicro A+ Server	Goldilocks v3.1	RedHat Enterprise
Technology Association	1124US-TNRP	Standard Edition	Linux 8.3

#### **TPC Benchmark™ C Metrics**

Total System Cost	TPC-C Throughput	Price/Performance	Availability Date
₩ 511,019,300 (KRW)	507,802 tpmC	1,006.34 KRW/tpmC	Available Now

## Preface

The Transaction Processing Performance Council ( $TPC^{TM}$ ) is a non-profit corporation founded to define transaction processing and database benchmarks and to disseminate objective, verifiable TPC performance data to the industry. The TPC Benchmark© C is an on-line transaction processing benchmark (OLTP) developed by the TPC.

#### TPC Benchmark™ C Overview

TPC Benchmark<sup>TM</sup> C (TPC-C) simulates a complete computing environment where a population of users executes transactions against a database. The benchmark is centered around the principal activities (transactions) of an order-entry environment. These transactions include entering and delivering orders, recording payments, checking the status of orders, and monitoring the level of stock at the warehouses. While the benchmark portrays the activity of a wholesale supplier, TPC-C is not limited to the activity of any particular business segment, but, rather represents any industry that must manage, sell, or distribute a product or service.

TPC-C consists of a mixture of read-only and update intensive transactions that simulate the activities found in complex OLTP application environments. It does so by exercising a breadth of system components associated with such environments, which are characterized by:

- The simultaneous execution of multiple transaction types that span a breadth of complexity
- On-line and deferred transaction execution modes
- Multiple on-line terminal sessions
- Moderate system and application execution time
- Significant disk input/output
- Transaction integrity (ACID properties)
- Non-uniform distribution of data access through primary and secondary keys
- Databases consisting of many tables with a wide variety of sizes, attributes, and relationships
- Contention of data access and update

The performance metric reported by TPC-C is a "business throughput" measuring the number of orders processed per minute. Multiple transactions are used to simulate the business activity of processing an order, and each transaction is subject to a response time constraint. The performance metric for this benchmark is expressed in transactions-per-minute-C (tpmC). To be compliant with the TPC-C standard, all references to tpmC results must include the tpmC rate, the associated price-per-tpmC, and the availability date of the priced configuration.

TPC-C uses terminology and metrics that are similar to other benchmarks, originated by the TPC or others. Such similarity in terminology does not in any way imply that TPC-C results are comparable to other benchmarks. The only benchmark results comparable to TPC-C are other TPC-C results conformant with the same revision.

Despite the fact that this benchmark offers a rich environment that emulates many OLTP applications, this benchmark does not reflect the entire range of OLTP requirements. In addition, the extent to which a customer can achieve the results reported by a vendor is highly dependent on how closely TPC-C approximates the customer application. The relative performance of systems derived from this benchmark does not necessarily hold for other workloads or environments. Extrapolations to other environments are not recommended.

Benchmark results are highly dependent upon workload, specific application requirements, and systems design and implementation. Relative system performance will vary as a result of these and other factors. Therefore, TPC-C should not be used as a substitute for a specific customer application benchmark when critical capacity planning and/or product evaluation decisions are contemplated.

Further information is available at www.tpc.org





## Goldilocks v3.1 Standard Edition on Supermicro A+ Server 1124US-TNRP

TPC-C Version 5.11.0 TPC Pricing 2.8.0 Report Date 31-Aug-2020

> Availability Date Available Now

Description	Part Number	Sou	Unit Price	Qty	Price	3-Yr. Maint.
Server Hardware		rce				Price
1 x DB Server						
Supermicro A+ Server 1124US-TNRP	AS-1124US-TNRP	1	108,500,000	1	251,400,000	
AMD EPYC 7343 16-Core Processor	PSE-MLN7343-0338	1	(included)	2		
Samsung M386ABG40M50-CYF 256GB LRDIMM Memory	MEM-DR425L-SL01-LR29	1	(included)	32		
KIOXIA KCD6XLUL7T68 7.68TB NVMe SSD	HDS-TUN0- KCD6XLUL7T68	1	(included)	2		
Samsung SAMSUNG MZ7LH7T6HMLA-00005 7.68TB SATA SSD	MZ7LH7T6HMLA-00005-2	1	(included)	6		
Broadcom/LSI MegaRAID SAS-3 3108	AOC-S3108L-H8IR	1	(included)	1		
2-port 25GbE SFP28 Mellanox CX-4 Lx EN	AOC-MCX4121A-ACAT	1	(included)	1		
ASSEMBLY FEE	MC0037	1	(included)	1		
Maintenance - 7x24x4 Care Pack (3-yrs)	OS4HR3	1	(included)	1		
2 x WAS Servers (per server)						
Supermicro A+ Server 1114S-WN10RT	AS-1114S-WN10RT	1	19,200,000	2	38,400,000	
AMD EPYC 7713 64-Core	PSE-MLN7713-0344	1	(included)	1		
Supermicro 16GB 288-Pin DDR4 2666MHz	MEM-DR416L-CL06-ER32	1	(included)	8		
1.92TB NVMe U.2 2.5" PCIe 4.0 x4 SSD	HDS-SUN0- MZQL21T9HCJRA7	1	(included)	2		
2-port 100GbE SFP28 Mellanox CX-5	AOC-MCX516A-CDAT	1	(included)	1		
ASSEMBLY FEE	MC0037	1	(included)	1		
Maintenance - 7x24x4 Care Pack (3-yrs)	OS4HR3	1	(included)	1		
1 x WAS Servers (per server)						
Supermicro A+ Server 1114S-WN10RT	AS-1114S-WN10RT	1	19,200,000	1	19,200,000	
AMD EPYC 7713 64-Core	PSE-MLN7713-0344	1	(included)	1		
Supermicro 16GB 288-Pin DDR4 2666MHz	MEM-DR416L-CL06-ER32	1	(included)	8		
Intel DC P4511 1TB NVMe PCIe 3.1 x4, M.2 SSD	HDS-IMN0- SSDPELKX010T8	1	(included)	1		
2-port 25GbE SFP28 Mellanox CX-4 Lx EN	AOC-MCX4121A-ACAT- MLN	1	(included)	1		
2-port 100GbE SFP28 Mellanox CX-5	AOC-MCX516A-CDAT	1	(included)	1		
ASSEMBLY FEE	MC0037	1	(included)	1		
Maintenance - 7x24x4 Care Pack (3-yrs)	OS4HR3	1	(included)	1		
Server Hardware Sub Total					309,000,000	
Network						
Mellanox MSN2700-CS2F Spectrum 100GbE 1U Open Ethernet Switch	MSN2700-CS2F	2	36,844,000	1	36,844,000	
Mellanox ETH 100GbE to 4x25GbE, QSFP28 to 4xSFP28, 1m, Colored, 30AWG, CA-N	MCP7F00-A001R30N	2	234,000	1	234,000	
Technical Support and Warranty - 24x7, 4 Hours On-Site Support 1yr	Maintenance	2	3,770,000	3		11,310,000

© 2022 Telecommunications Technology Association. All rights reserved.

Network Sub Total						37,078,000	11,310,000
Client/Server Software							
Red Hat Enterprise Linux Server Standard 3yrs	RH0000	)4	3	4,098,000	4	16,392,000	
RHEL Server Standard Maintenance - 3yrs 24x7x4hrs	RP-CPS(O		3	6,000,000	4		24,000,000
Red Hat JBoss Web Server 16-Core Standard 3Year	MW02900	56F3	3	28,005,000	3	84,015,000	
JBoss Web Server per 16Core 3Year Maintenance	RP-CPS(W)	AS)F3	3	30,000,000	3		90,000,000
Goldilocks v3.1 Standard Edition	-		4	64,000,000	1	64,000,000	
Goldilocks v3.1 Standard Edition Technical Supports	-		4	20,000,000	3		60,000,000
Software Sub Total						164,407,000	174,000,000
Infrastructure							
HP M27fw FHD 27-inch Monitor (w/ spares)	2H1B1A	A	5	199,000	3	597,000	
HP 235 Wireless Keyboard and Mouse (w/ spares)	1Y4D0A	A	5	34,900	3	104,700	
Infrastructure Sub Total						701,700	
Discounts*							
Red Hat Enterprise Linux Server Standard						(6,232,000)	(12,000,000)
Red Hat Jboss Web Server 16 Core Standard						(30,245,400)	(45,000,000)
Goldilocks v3.1 Standard Edition						(41,600,000)	(50,400,000)
Discounts Sub Total						(78,077,400)	(107,400,000)
Total						433,109,300	77,910,000
Pricing Notes         1)Super Solution Co.,LTD       4) Sunjesoft In         2) ICRAFT Co., LTD       5) Hewlett Pace         3) Rockplace Inc.       5) Hewlett Pace         All of the prices are based on South Korea's configurations and excluded VAT.         * All discounts are based on Korea list prices quantities and configurations. Discounts for configurations will be similar to those quoted vary based on the components in the configuration	currency, KRW and for similar similarly sized here, but may	Three y	/ear co		-C th	RW(₩): { roughput: 50 nce: 1,006.3	

#### Benchmark implementation and results independantly audited by Doug Johnson of InfoSizing (www.sizing.com)

Prices used in TPC benchmarks reflect the actual prices a customer would pay for a one-time purchase of the stated components. Individually negotiated discounts are not permitted. Special prices based on assumptions about past or future purchases are not permitted. All discounts reflect standard pricing policies for the listed components. For complete details, see the pricing sections of the TPC benchmark pricing specifications. If you find that the stated prices are not available according to these terms, please inform the TPC at pricing@tpc.org. Thank you.



## Goldilocks v3.1 Standard Edition on Supermicro A+ Server 1124US-TNRP

TPC-C Version 5.11.0 TPC Pricing 2.8.0 Report Date 31-Aug-2020

Availability Date

Available Now

MQTh, computed Maximum Qualified Throughput	507,802 tpmC	
· · · · · · · · · · · · · · · · · · ·	, I	

Response Times (seconds)	Min	Average	90 <sup>th</sup>	Мах
New-Order	0.101	0.104	0.107	0.588
Payment	0.101	0.103	0.106	0.596
Order-Status	0.101	0.103	0.106	0.580
Delivery (interactive portion)	0.101	0.101	0.102	0.561
Delivery (deferred portion)	0.001	0.018	0.045	0.871
Stock-Level	0.102	0.104	0.106	0.550
Menu	0.101	0.102	0.103	0.617

Emulated Display Delay: 0.1 sec.

Transaction Mix	Percent	Number
New-Order	44.980%	213,277,203
Payment	43.011%	203,943,214
Order-Status	4.002%	18,977,456
Delivery	4.004%	18,983,206
Stock-Level	4.003%	18,981,402

Keying Times (seconds)	Min	Average	Max
New-Order	18.001	18.001	18.006
Payment	3.001	3.001	3.007
Order-Status	2.001	2.001	2.006
Delivery	2.001	2.001	2.005
Stock-Level	2.001	2.001	2.006

Think Times (seconds)	Min	Average	Max
New-Order	0.001	12.044	120.501
Payment	0.001	12.043	120.502
Order-Status	0.001	10.048	100.501
Delivery	0.001	5.030	50.301
Stock-Level	0.001	5.031	50.301

Test Duration	
Ramp-up time	65 min
Measurement Interval (MI)	420 min
Checkpoints in MI	16
Checkpoint Interval (Average / Max)	25.02 min / 25.11 min
Number of Transactions in MI (all types)	474,162,481

<sup>© 2022</sup> Telecommunications Technology Association. All rights reserved.

## **General Items**

## 0.1 Application Code and Definition Statements

The application program (as defined in clause 2.1.7) must be disclosed. This includes, but is not limited to, the code implementing the five transactions and the terminal input output functions.

Appendix A contains the application source code for the transactions.

#### 0.2 Benchmark Sponsor

A statement identifying the benchmark sponsor(s) and other participating companies must be provided.

This benchmark was sponsored by TTA, Telecommunications Technology Association. The implementation was developed and engineered with SUNJESOFT Inc. and Super Micro Computer, Inc.

## 0.3 Parameter Settings

Settings must be provided for all customer-tunable parameters and options which have been changed from the defaults found in actual products, including by not limited to:

- Database options
- Recover/commit options
- Consistency locking options
- Operating system and application configuration parameters

This requirement can be satisfied by providing a full list of all parameters.

Appendix B contains the tunable parameters for the database, the operating system, and the transaction monitor.

## 0.4 Configuration Diagrams

Diagrams of both measured and priced configurations must be provided, accompanied by a description of the differences.

The configuration diagram for both the tested and priced system is depicted in Figure 0.1. There was no difference between the priced and tested configurations.



#### Figure 0.1: Benchmarked and Priced Configuration

# **Clause 1: Logical Database Design**

## 1.1 Table Definitions

*Listing must be provided for all table definition statements and all other statements used to set up the database.* Appendix A contains the code used to define and load the database tables.

## **1.2 Physical Organization of Database**

*The physical organization of tables and indices within the database must be disclosed.* The physical organization of the database is shown in Table 1.2.

Controller	Array	RAID Array	Drives	Content
MegaRAID SAS-3 3108	Internal	RAID 1	2 x 7.68TB SATA SSD	OS
MegaRAID SAS-3 3108	Internal	RAID 10	4 x 7.68TB SATA SSD	Redo Logs
Software RAID	Internal	RAID 1	2 x 7.68TB NVMe SSD	Data Files

#### Table 1.2: Physical Organization of the Database

## **1.3 Insert and Delete Operations**

It must be ascertained that insert and/or delete operations to any of the tables can occur concurrently with the TPC-C transaction mix. Furthermore, any restrictions in the SUT database implementation that precludes inserts beyond the limits defined in Clause 1.4.11 must be disclosed. This includes the maximum number of rows that can be inserted and the minimum key value for these new rows.

All insert and delete functions were verified to be fully operational during the entire benchmark.

## 1.4 Horizontal or Vertical Partitioning

While there are a few restrictions placed upon horizontal or vertical partitioning of tables and rows in the TPC-C benchmark, any such partitioning must be disclosed.

No horizontal or vertical partitioning was used in this benchmark.

## **1.5 Replication or Duplication**

Replication of tables, if used, must be disclosed. Additional and/or duplicated attributes in any table must be disclosed along with a statement on the impact on performance.

No replications, duplications or additional attributes were used in this benchmark.

## **Clause 2: Transaction and Terminal Profiles**

### 2.1 Random Number Generation

The method of verification for the random number generation must be described.

Random numbers were generated using 'SysVr4 rand\_r()' call. The seed value for 'rand\_r()' was collected and reviewed by the auditor.

### 2.2 Input/Output Screens

The actual layout of the terminal input/output screens must be disclosed.

All screen layouts were verified by the auditor to validate that they followed the requirements of the specifications.

## 2.3 Priced Terminal Feature

The method used to verify that the emulated terminals provide all the features described in Clause 2.2.2.4 must be explained. Although not specifically priced, the type and model of the terminals used for the demonstration in 8.1.3.3 must be disclosed and commercially available (including supporting software and maintenance).

The terminal attributes were manually verified by the auditor by verifying that each required feature was implemented.

### 2.4 Presentation Managers

Any usage of presentation managers or intelligent terminals must be explained.

Application code running on the client systems implemented the TPC-C user interface. No presentation manager software or intelligent terminal features were used. The source code for the user interface is listed in Appendix A.

## **2.5 Transaction Statistics**

Table 2.1 lists the transaction statistics defined in Clauses 8.1.3.5 to 8.1.3.11 and observed during the Measurement Interval.

	Value	
New Order	Home warehouse order lines Remote warehouse order lines Rolled back transactions Average items per order	99.001% 0.999% 1.002% 10.000
Payment	Home warehouse Remote warehouse Accessed by last name	85.001% 14.999% 59.993%
Order Status	Accessed by last name	60.004%
Delivery	Skipped transactions	0
Transaction Mix	New Order Payment Order status Delivery Stock level	44.980% 43.011% 4.002% 4.004% 4.003%

 Table 2.1: Transaction Statistics

## 2.6 Queuing Mechanism

The queuing mechanism used to defer the execution of the Delivery transaction must be disclosed. The queuing mechanism was implemented using 'BlockingQueue' provided by Java.

## **Clause 3: Transaction and System Properties**

The results of the ACID tests must be disclosed along with a description of how the ACID requirements were met. This includes disclosing which case was followed for the execution of Isolation Test 7.

All ACID property tests were conducted according to the specification.

## 3.1 Atomicity

The system under test must guarantee that the database transactions are atomic; the system will either perform all individual operations on the data or will assure that no partially completed operations leave any effects on the data.

#### 3.1.1 Atomicity of Completed Transactions

Perform the Payment transaction for a randomly selected warehouse, district, and customer (by customer number) and verify that the records in the CUSTOMER, DISTRICT, and WAREHOUSE tables have been changed appropriately.

A row was randomly selected from the CUSTOMER, DISTRICT, and WAREHOUSE tables, and the balances noted. A payment transaction was started with the same Customer, District, and Warehouse identifiers and a known amount. The payment transaction was committed and the rows were verified to contain correctly updated balances.

#### 3.1.2 Atomicity of Aborted Transactions

Perform the Payment transaction for a randomly selected warehouse, district, and customer (by customer number) and substitute a ROLLBACK of the transaction for the COMMIT of the transaction. Verify that the records in the CUSTOMER, DISTRICT, and WAREHOUSE tables have NOT been changed.

A row was randomly selected from the CUSTOMER, DISTRICT, and WAREHOUSE tables, and the balances noted. A payment transaction was started with the same Customer, District, and Warehouse identifiers and a known amount. The payment transaction was rolled back and the rows were verified to contain the original balances.

#### 3.2 Consistency

Consistency is the property of the application that requires any execution of a data base transaction to take the database from one consistent state to another, assuming that the data base is initially in a consistent state.

Verify that the data base is initially consistent by verifying that it meets the consistency conditions defined in Clauses 3.3.2.1 to 3.3.2.4. Describe the steps used to do this in sufficient detail so that the steps are independently repeatable.

The specification defines 12 consistency conditions, of which Consistency conditions 1 through 4 were demonstrated as follows:

- 1. The sum of balances (d\_ytd) for all Districts within a specific Warehouse is equal to the balance (w\_ytd) of that Warehouse.
- 2. For each District within a Warehouse, the next available Order ID (d\_next\_o\_id) minus one is equal to the most recent Order ID [max(o\_id)] for the ORDER table associated with the preceding District and Warehouse. Additionally, that same relationship exists for the most recent Order ID [max(o\_id)] for the NEW-ORDER table associated with the same District and Warehouse. Those relationships can be illustrated as:

3. For each District within a Warehouse, the value of the most recent Order ID [max(no\_o\_id)] minus the first Order ID [min(no\_o\_id)] plus one, for the NEW-ORDER table associated with the District and Warehouse, equals the number of rows in that NEW-ORDER table. That relationship can be illustrated as:

max(no\_o\_id) - min(no\_o\_id) + 1 = rows in NEW-ORDER
 where (o\_w\_id = no\_w\_id) and (o\_d\_id = no\_d\_id)

4. For each District within a Warehouse, the sum of Order-Line counts [sum(o\_ol\_cnt)] for the Orders associated with the District equals the number of rows in the ORDER-LINE table associated with the same District.

That relationship can be illustrated as:

sum(o\_ol\_cnt) = rows in the ORDER-LINE table for the Warehouse and District

To test consistency, the following steps were executed:

- 1. The consistency conditions 1 through 4 were tested by running queries against the database. All queries showed that the database was in a consistent state.
- 2. An RTE run was executed at full load for a duration sufficient to include at least one completed checkpoint.
- 3. The consistency conditions 1 through 4 were tested again. All queries showed that the database was still in a consistent state.

#### 3.3 Isolation

Sufficient conditions must be enabled at either the system or application level to ensure the required isolation defined above (clause 3.4.1) is obtained.

The benchmark specification defines nine tests to demonstrate the property of transaction isolation. The tests, described in Clauses 3.4.2.1 - 3.4.2.9, were all successfully executed using a series of scripts. Each included timestamps to demonstrate the concurrency of operations. The results of the queries were logged. The captured logs were verified to demonstrate the required isolation had been met.

#### **Isolation Test 1**

This test demonstrates isolation for read-write conflicts of Order-Status and New-Order transactions when the New-Order transaction is committed.

- 1. An Order-Status transaction T0 was executed and committed for a randomly selected Customer, and the Order returned was noted.
- 2. A New-Order transaction T1 was started for the same Customer used in T0. T1 was stopped prior to COMMIT.
- 3. An Order-Status transaction T2 was started for the same Customer used in T1. T2 completed and was committed without being blocked by T1. T2 returned the same Order that T0 had returned.
- 4. T1 was allowed to complete and was committed.
- 5. An Order-Status transaction T3 was started for the same Customer used in T1. T3 returned the Order inserted by T1.

#### Isolation Test 2

This test demonstrates isolation for read-write conflicts of Order-Status and New-Order transactions when the New-Order transaction is rolled back.

The test proceeds as follows:

- 1. An Order-Status transaction T0 was executed and committed for a randomly selected Customer and the Order returned was noted.
- 2. A New-Order transaction T1 with an invalid item number was started for the same Customer used in T0. T1 was stopped immediately prior to ROLLBACK.
- 3. An Order-Status transaction T2 was started for the same Customer used in T1. T2 completed and was committed without being blocked by T1. T2 returned the same Order that T0 had returned.
- 4. T1 was allowed to ROLLBACK.
- 5. An Order-Status transaction T3 was started for the same Customer used in T1. T3 returned the same Order that T0 had returned.

#### **Isolation Test 3**

This test demonstrates isolation for write-write conflicts of two New-Order transactions when both transactions are committed.

The test proceeds as follows:

- 1. The D\_NEXT\_O\_ID of a randomly selected district was retrieved.
- 2. A New-Order transaction T1 was started for a randomly selected customer within the District used in step 1. T1 was stopped immediately prior to COMMIT.
- 3. Another New-Order transaction T2 was started for the same customer used in T1. T2 waited.
- 4. T1 was allowed to complete. T2 completed and was committed.
- 5. The order number returned by T1 was the same as the D\_NEXT\_O\_ID retrieved in step 1. The order number returned by T2 was one greater than the order number returned by T1.
- 6. The D\_NEXT\_O\_ID of the same District was retrieved again. It had been incremented by two (i.e. it was one greater than the order number returned by T2).

#### **Isolation Test 4**

This test demonstrates isolation for write-write conflicts of two New-Order transactions when one transaction is rolled back.

- 1. The D\_NEXT\_O\_ID of a randomly selected District was retrieved.
- 2. A New-Order transaction T1, with an invalid item number, was started for a randomly selected customer within the district used in step 1. T1 was stopped immediately prior to ROLLBACK.
- 3. Another New-Order transaction T2 was started for the same customer used in T1. T2 waited.
- 4. T1 was allowed to roll back, and T2 completed and was committed.
- 5. The order number returned by T2 was the same as the D\_NEXT\_O\_ID retrieved in step 1.

6. The D\_NEXT\_O\_ID of the same District was retrieved again. It had been incremented by one (i.e. one greater than the order number returned by T2).

#### Isolation Test 5

This test demonstrates isolation for write-write conflicts of Payment and Delivery transactions when Delivery transaction is committed.

The test proceeds as follows:

- 1. A query was executed to find out the Customer who is to be updated by the next Delivery transaction for a randomly selected Warehouse and District.
- 2. The C\_BALANCE of the Customer found in step 1 was retrieved.
- 3. A Delivery transaction T1 was started for the same Warehouse used in step 1. T1 was stopped immediately prior to COMMIT.
- 4. A Payment transaction T2 was started for the same Customer found in step 1. T2 waited.
- 5. T1 was allowed to complete. T2 completed and was committed.
- 6. The C\_BALANCE of the Customer found in step 1 was retrieved again. The C\_BALANCE reflected the results of both T1 and T2.

#### **Isolation Test 6**

This test demonstrates isolation for write-write conflicts of Payment and Delivery transactions when the Delivery transaction is rolled back.

The test proceeds as follows:

- 1. A query was executed to find out the Customer who is to be updated by the next delivery transaction for a randomly selected Warehouse and District.
- 2. The C\_BALANCE of the Customer found in step 1 was retrieved.
- 3. A Delivery transaction T1 was started for the same Warehouse used in step 1. T1 was stopped immediately prior to COMMIT.
- 4. A Payment transaction T2 was started for the same customer found in step 1. T2 waited.
- 5. T1 was forced to execute a ROLLBACK. T2 completed and was committed. The C\_BALANCE of the Customer found in step 1 was retrieved again. The C\_BALANCE reflected the results of only T2.

#### Isolation Test 7

This test demonstrates repeatable reads for the New-Order transaction while an interactive transaction updates the prices of some items.

- 1. The I\_PRICE of two randomly selected items X and Y were retrieved.
- 2. A New-Order transaction T1 with a group of Items including Items X and Y was started. T1 was stopped immediately after retrieving the prices of all items. The prices of Items X and Y retrieved matched those retrieved in step 1.

- 3. A transaction T2 was started to increase the price of Items X and Y by 10%.
- 4. T2 did not stall and was committed.
- 5. T1 was resumed, and the prices of all Items were retrieved again within T1. The prices of Items X and Y matched those retrieved in step 1.
- 6. T1 was committed.
- 7. The prices of Items X and Y were retrieved again. The values matched the values set by T2.

The Execution followed Case D, where T3 does not stall and no transaction is rolled back. Query T4 verifies the price change made by T3.

#### **Isolation Test 8**

This test demonstrates isolation for phantom protection between New-Order and Delivery transactions.

The test proceeds as follows:

- 1. The NO\_D\_ID of all NEW\_ORDER rows for a randomly selected Warehouse and District was changed to 11. The changes were committed.
- 2. A Delivery transaction T1 was started for the selected Warehouse.
- 3. T1 was stopped immediately after reading the NEW\_ORDER table for the selected Warehouse and District. No qualifying row was found.
- 4. A New-Order transaction T2 was started for the same Warehouse and District. T2 completed and was committed without being blocked by T1.
- 5. T1 was resumed and the NEW\_ORDER table was read again. No qualifying row was found.
- 6. T1 completed and was committed.
- 7. The NO\_D\_ID of all NEW\_ORDER rows for the selected Warehouse and District was restored to the original value. The changes were committed.

### Isolation Test 9

This test demonstrates isolation for phantom protection between New-Order and Order-Status transactions.

- 1. An Order-Status transaction T1 was started for a randomly selected Customer.
- 2. T1 was stopped immediately after reading the ORDER table for the selected Customer to find the most recent Order for that Customer.
- 3. A New-Order transaction T2 was started for the same Customer. T2 completed and was committed without being blocked by T1.
- 4. T1 was resumed and the ORDER table was read again to determine the most recent Order for the same Customer. The Order found was the same as the one found in step 2.
- 5. T1 completed and was committed.

## 3.4 Durability

The tested system must guarantee durability: the ability to preserve the effects of committed transactions and ensure data base consistency after recovery from any one of the failures listed in Clause 3.5.3

- Permanent irrecoverable failure of any single durable medium containing TPC-C database tables or recovery log data (this test includes failure of all or part of memory)
- Instantaneous interruption (system crash/system hang) in processing that requires system reboot to recover
- Failure of all or part of memory (loss of contents)

#### 3.4.1 Durable Media Failure

#### 3.4.1.1 Loss of Log Media and Data Media

This test was conducted on a fully scaled database. To demonstrate recovery from a permanent failure of durable medium containing TPC-C Log Media and Data Media, the following steps were executed:

- 1. The total number of Orders is determined by the sum of D\_NEXT\_O\_ID of all rows in the DISTRICT table; giving count-1.
- 2. The consistency is verified.
- 3. The RTE is started with full user load.
- 4. The test is allowed to run for a minimum of 5 minutes after ramp-up.
- 5. A first checkpoint is initiated and completed.
- 6. The test is allowed to run for a minimum of 2 more minutes.
- 7. A second checkpoint is initiated.
- 8. Before the second checkpoint completes, one data disk is disabled by removing it logically. Since the data disks are configured with redundancy, the transactions continued to run without interruption.
- 9. The test is allowed to run until the completion of the second checkpoint and for at least 5 minutes
- 10. A third checkpoint is initiated.
- 11. Before the third checkpoint completes, one log device is disabled by removing it logically. Since the log devices are configured with redundancy, the transactions continued to run without interruption.
- 12. The test is allowed to run until the third checkpoint has completed, but no less than 5 more minutes.
- 13. The RTE run is completed.
- 14. The consistency is verified.
- 15. Step 1 is repeated, giving count-2.
- 16. The RTE result file is used to determine the number of New-Order transactions successfully completed during the full run.
- 17. The difference between the count-1 and count-2 is compared with the number of New-Order transactions successfully completed during the full run. The difference indicated that no committed transactions had been lost.
- 18. Data from the success file is used to query the database to demonstrate that the last 500 successful New-Orders have corresponding rows in the ORDER table.

#### 3.4.2 Instantaneous Interruption, Loss of Memory

As the loss of power erases the contents of memory, the instantaneous interruption and the loss of memory tests were combined into a single test. This test was executed on a fully scaled database. The following steps were executed:

- 1. The total number of Orders is determined by the sum of D\_NEXT\_O\_ID of all rows in the DISTRICT table; giving count-1.
- 2. The consistency is verified.
- 3. The RTE is started with full user load.
- 4. The test is allowed to run for a minimum of 5 minutes at full load (after ramp-up).
- 5. A first checkpoint is initiated and completed.
- 6. The test is allowed to run for a minimum of 2 more minutes.
- 7. A second checkpoint is initiated.
- 8. Before the second checkpoint completes, the primary power is shut off logically using BMC
- 9. The RTE is shutdown.
- 10. Power is restored to the database server and the system performs an automatic recovery.
- 11. GOLDILOCKS is restarted and performs an automatic recovery.
- 12. Step 1 is repeated, giving count-2.
- 13. The consistency is verified.
- 14. The RTE result file is used to determine the number of New-Order transactions successfully completed during the full run.
- 15. The difference between the count-1 and count-2 is compared with the number of New-Order transactions successfully completed during the full run. The difference indicated that all committed transactions had been successfully recovered.
- 16. Data from the success file is used to query the database to demonstrate that the last 500 successful New-Orders have corresponding rows in the ORDER table.

## **Clause 4: Scaling and Database Population**

### 4.1 Cardinality of Tables

The cardinality (e.g. number of rows) of each table, as it existed at the start of the benchmark run, must be disclosed. If the database was over-scaled and inactive rows of the WAREHOUSE table were deleted, the cardinality of the WAREHOUSE table as initially configured and the number of rows

Table 4.1 shows that number of rows for each table as they were initially populated.

Table	Cardinality
Warehouse	40,000
District	400,000
Customer	1,200,000,000
History	1,200,000,000
Order	1,200,000,000
New Order	360,000,000
Order Line	11,997,194,546
Stock	4,000,000,000
Item	100,000
Unused Warehouses	0

#### Table 4.1: Number of Rows for Server

### 4.2 Database Implementation

A statement must be provided that describes: The data model implemented by DBMS used (e.g. relational, network, hierarchical). The database interfaces (e.g. embedded, call level) and access language (e.g. SQL, DL/1, COBOL read/write used to implement the TPC-C transaction. If more than one interface/access language is used to implement TPC-C, each interface/access language must be described and a list of which interface/access language is used with which transaction type must be disclosed.

Goldilocks v3.1 is an in-memory DBMS, implementing the relational model.

The transactions are implemented in SQL via JDBC calls to the database engine.

All application code and procedures are listed in Appendix A.

## 4.3 Distribution of Database Files

The distribution of tables and logs across all media must be explicitly depicted for tested and priced systems.

The database files are stored on a set of two 7.68TB disks configured as RAID1. The database log files are stored on four 7.68TB disks configured as RAID10.

Name	Location	Description
system_XXX.dbf	/data/db/db1	System tables and dictionary
tpcc_data_XXX.dbf	/data/db/db1 /data/db/db2 /data/db/db3 /data/db/db4 /data/db/db5	Database data files
redo_X_X.log	/wal	Database log files

Table 4.3: Database file locations

The distribution of tables and logs across storage media is shown in Table 1.2.

### 4.4 60-Day Space

Details of the 60-day space computations along with proof that the database is configured to sustain 8 hours of growth for the dynamic tables (Order, Order-Line, and History) must be disclosed.

A test run of over 8 hours was executed to demonstrate that the configuration is capable of sustaining 8 hours of growth at the reported throughput. The computation of the 60-day storage requirements is shown in Table 4.4.

Base Unit (KBytes)	1						
tpmC	507,802.864						
Table	Rows	Data	Index	Initial Population 5	% Growth	8-Hour Growth	Required Runtime Space
WAREHOUSE	40,000	322,496	1,096	323,592	16,180	0	339,772
DISTRICT	400,000	51,040	15,776	66,816	3,341	0	70,157
CUSTOMER	1,200,000,000	772,505,232	100,411,584	872,916,816	43,645,841	0	916,562,657
NEW_ORDER	360,000,000	22,724,512	12,993,048	35,717,560	1,785,878	0	37,503,438
ITEM	100,000	10,808	2,752	13,560	678	0	14,238
STOCK	4,000,000,000	1,476,738,032	134,104,128	1,610,842,160	80,542,108	0	1,691,384,268
HISTORY	1,200,000,000	100,541,384	0	100,541,384	0	20,422,081	120,963,465
ORDERS	1,200,000,000	76,666,848	92,658,824	169,325,672	0	15,572,658	184,898,330
ORDER_LINE	11,997,194,546	1,145,321,560	477,829,640	1,623,151,200	0	232,639,027	1,855,790,227
Total		3,594,881,912	818,016,848	4,412,898,760	125, <mark>994</mark> ,025	268,633,766	4,807,526,552
60-Day Req	uirements		Memory Re	quirements		Storage	Requirements
Dynamic-Space	1,322,529,792		Final Allocation	4,894,793,720		Total Disk Space	23,043,254,518
Free-Space	8,245,160		Non-Growing 5%	125,994,025			
Static-Space	3,090,368,968					Log space used	692,060,160
						60-Day Space	19,208,394,954
Daily-Growth	268,633,766					8 Å.	
Daily-Spread	0					Remaining Space	3,142,799,403
60-Day Space	19,208, <mark>394,9</mark> 54		1-Day Memory	5,020,787,745			

#### Table 4.4: 60-Day Space Calculations

## **Clause 5: Performance Metrics**

## 5.1 TPC Benchmark C Metrics

The TPC-C Metrics are reported in the front of this report as part of the executive summary.

## 5.2 Response Times

Ninetieth percentile, maximum and average response times must be reported for all transaction types as well as for the menu response time.

During the performance run transactions are submitted by the RTE in accordance with the required mix, Keying Times and Think Times of the benchmark Specification. Transactions are submitted by emulated users via HTTP. All timings are recorded by the RTE. The response time is measured from the submission of the transaction until the last byte of response is received by the RTE.

The details of the response times are reported in the front of this report as part of the Executive Summary.

## 5.3 Keying and Think Times

The minimum, the average, and the maximum keying and think times must be reported for each transaction type.

The details of the keying and think times are reported in the front of this report as part of the Executive Summary.

## 5.4 Distribution and Performance Curves

#### 5.4.1 Response Time frequency distribution curves

Response Time frequency distribution curves must be reported for each transaction type.

Figure 5.4.1.1 shows the Response Time frequency distribution curves for the New-Order transaction.

Figure 5.4.1.2 shows the Response Time frequency distribution curves for the Payment transaction.

Figure 5.4.1.3 shows the Response Time frequency distribution curves for the Order-Status transaction.

Figure 5.4.1.4 shows the Response Time frequency distribution curves for the interactive portion of the Delivery transaction.

Figure 5.4.1.5 shows the Response Time frequency distribution curves for the Stock-Level transaction.



Figure 5.4.1.1: New-Order RT Frequency Distribution



Figure 5.4.1.2: Payment RT Frequency Distribution



Figure 5.4.1.3: Order-Status RT Frequency Distribution



Figure 5.4.1.4: Delivery (Interactive) RT Frequency Distribution



Figure 5.4.1.5: Stock-Level RT Frequency Distribution

#### 5.4.2 Response Time versus throughput

The performance curve for response times versus throughput must be reported for the New-Order transaction.

Figure 5.4.2 shows the Response Time versus throughput curves for the New-Order transaction.



Figure 5.4.2: New-Order RT versus Throughput

#### 5.4.3 Think Time frequency distribution

Think Time frequency distribution curves (see Clause 5.6.3) must be reported for the New-Order transaction.

Figure 5.4.3 shows the Think Time frequency distribution curves for the New-Order transaction.



Figure 5.4.3: New-Order Think Time Frequency Distribution

#### 5.4.4 Throughput versus elapsed time

A graph of throughput versus elapsed time must be reported for the New-Order transaction.

Figure 5.4.4 shows the throughput versus elapsed time for the New-Order transaction. The start and end of the Measurement Interval is included on the figure.



Figure 5.4.4: New-Order Throughput versus Elapsed Time

## 5.5 Steady State Determination

The method used to determine that the SUT had reached a steady state prior to commencing the measurement interval must be disclosed.

Steady state was determined using real time monitor utilities from the RTE. Steady state was further confirmed by a visual analysis of the throughput graph.

## 5.6 Work Performed During Steady State

A description of how the work normally performed during a sustained test (for example checkpointing, writing redo/undo log records, etc.) actually occurred during the measurement interval must be reported.

During the test, Goldilocks satisfied all of the ACID properties required by the benchmark specification. Committed transactions write a Redo record in the transaction log, to be used in case of system failure. The Redo records are used for roll-forward recovery during a re-start following a failure. This prevents the system from losing any committed transactions. Checkpoints periodically occurred about every 25 min. and are completed in about 25 min.

### **5.7 Measurement Period Duration**

A statement of the duration of the measurement interval for the reported Maximum Qualified Throughput (tpmC) must be included.

The duration of the reported measured interval was 7 hours (7hr = 420min = 25,200sec).

### **5.8 Transaction Statistics**

The percentage of the total mix for each transaction type must be disclosed. The percentage of New-Order transactions rolled back as a result of invalid item number must be disclosed. The average number of order-lines entered per New-Order transaction must be disclosed. The percentage of remote order lines per New-Order transaction must be disclosed. The percentage of remote be disclosed. The percentage of customer selections by customer last name in the Payment and Order-Status transactions must be disclosed. The percentage of skipped Delivery transactions must be disclosed.

The details of the transaction statistics are reported in the front of this report as part of the Executive Summary.

## **5.9 Checkpoints**

The number of checkpoints in the Measurement Interval, the time in seconds from the start of the Measurement Interval to the first checkpoint, and the Checkpoint Interval must be disclosed.

Two full checkpoints occurred before the Measurement Interval. 15 full checkpoints occurred during the Measurement Interval. The checkpoints' start and end times and durations during the Measurement Interval are listed in table 5.6.

Event	Event time	Execution time	Interval
Measurement Interval Begin	2020-08-04 12:04:39	-	-
Checkpoint3 End	2022-08-04 12:14:41	00:25:04	
Checkpoint4 Begin	2022-08-04 12:14:41		00:25:04
Checkpoint4 End	2022-08-04 12:39:45	00:25:04	
Checkpoint5 Begin	2022-08-04 12:39:45		00:25:04
Checkpoint5 End	2022-08-04 13:04:40	00:24:55	
Checkpoint6 Begin	2022-08-04 13:04:40		00:24:55
Checkpoint6 End	2022-08-04 13:29:43	00:25:03	
Checkpoint7 Begin	2022-08-04 13:29:43		00:25:03
Checkpoint7 End	2022-08-04 13:54:46	00:25:03	
Checkpoint8 Begin	2022-08-04 13:54:46		00:25:03
Checkpoint8 End	2022-08-04 14:19:47	00:25:01	
Checkpoint9 Begin	2022-08-04 14:19:47		00:25:01
Checkpoint9 End	2022-08-04 14:44:42	00:24:55	
Checkpoint10 Begin	2022-08-04 14:44:42		00:24:55
Checkpoint10 End	2022-08-04 15:09:45	00:25:03	
Checkpoint11 Begin	2022-08-04 15:09:45		00:25:03
Checkpoint11 End	2022-08-04 15:34:45	00:25:00	
Checkpoint12 Begin	2022-08-04 15:34:45		00:25:00
Checkpoint12 End	2022-08-04 15:59:52	00:25:07	
Checkpoint13 Begin	2022-08-04 15:59:52		00:25:07
Checkpoint13 End	2022-08-04 16:24:51	00:24:59	
Checkpoint14 Begin	2022-08-04 16:24:51		00:24:59
Checkpoint14 End	2022-08-04 16:50:02	00:25:11	
Checkpoint15 Begin	2022-08-04 16:50:02		00:25:11
Checkpoint15 End	2022-08-04 17:15:06	00:25:04	
Checkpoint16 Begin	2022-08-04 17:15:06		00:25:04
Checkpoint16 End	2022-08-04 17:40:07	00:25:01	
Checkpoint17 Begin	2022-08-04 17:40:07		00:25:01
Checkpoint17 End	2022-08-04 18:05:04	00:24:57	
Checkpoint18 Begin	2022-08-04 18:05:04		00:24:57
Checkpoint18 End	2022-08-04 18:30:06	00:25:02	

## Table 5.6: Checkpoints

Checkpoint19 Begin	2022-08-04 18:30:06		00:25:02
Checkpoint19 End	2022-08-04 18:55:09	00:25:03	
Checkpoint20 Begin	2022-08-04 18:55:09		00:25:03
Measurement Interval End	2022-08-04 19:08:20	-	-

## **Clause 6: SUT, Driver and Communication**

## 6.1 Remote Terminal Emulator (RTE)

If the RTE is commercially available, then its inputs must be specified. Otherwise, a description must be supplied of what inputs (e.g., scripts) to the RTE had been used.

The RTE software used was internally developed. The RTE simulated web users. It generated random input data based on the benchmark requirements and recorded response times and other statistics for each transaction cycle.

### 6.2 Emulated Components

It must be demonstrated that the functionality and performance of the components being emulated in the Driver System are equivalent to the priced system. The results of the test described in Clause 6.6.3.4 must be disclosed.

No components were emulated by the driver system.

### 6.3 Functional Diagrams

A complete functional diagram of both the benchmark configuration and the configuration of the proposed (target) system must be disclosed. A detailed list of all hardware and software functionality being performed on the Driver System and its interface to the SUT must be disclosed.

The diagram in Figure 0.1 shows the tested and priced benchmark configurations.

#### 6.4 Networks

The network configuration of both the tested services and proposed (target) services which are being represented and a thorough explanation of exactly which parts of the proposed configuration are being replaced with the Driver System must be disclosed.

The bandwidth of the networks used in the tested/priced configuration must be disclosed.

The diagram in Figure 0.1 shows the network configuration between the components of the tested configuration. The RTE and the SUT are connected through a 25/100Gbit switch.

The network bandwidths are listed in Figure 0.1.

### 6.5 Operator Intervention

If the configuration requires operator intervention (see Clause 6.6.6), the mechanism and the frequency of this intervention must be disclosed.

No operator intervention is required to sustain eight hours at the reported throughput.
# **Clause 7: Pricing**

### 7.1 Hardware and Software Pricing

A detailed list of hardware and software used in the priced system must be reported. Each separately orderable item must have vendor part number, description, and release/revision level, and either general availability status or committed delivery date. If package-pricing is used, vendor part number of the package and a description uniquely identifying each of the components of the package must be disclosed. Pricing source and effective date(s) of price(s) must also be reported.

The details of the hardware and software are reported in the front of this report as part of the Executive Summary.

### 7.2 Three Year Price

The total 3-year price of the entire configuration must be reported, including: hardware, software, and maintenance charges. Separate component pricing is recommended. The basis of all discounts used must be disclosed.

The pricing details for this TPC-C result are reported in the front of this report as part of the Executive Summary.

### 7.3 Availability Dates

The committed delivery date for general availability (availability date) of products used in the price calculations must be reported. When the priced system includes products with different availability dates, the reported availability date for the priced system must be the date at which all components are committed to be available.

Category	Available
Server Hardware	Available Now
Server Software	Now (date of publication)
Goldilocks v3.1 Standard Edition	Now (date of publication)

# **Clause 8: Reporting**

## 8.1 Full Disclosure Report

A Full Disclosure report is required in order for results to be considered compliant with the TPC-C benchmark specification

This document constitute the Full Disclosure Report for the TPC-C benchmark result describes within.

# **Clause 9: Auditor Attestation**

### **9.1 Auditor Information**

The auditor's agency name, address, phone number, and Attestation letter with a brief audit summary report indicating compliance must be included in the full disclosure report. A statement should be included specifying who to contact in order to obtain further information regarding the audit process.

This benchmark was audited by:

InfoSizing

Doug Johnson 63 Lourdes Drive Leominster, MA, 01453 USA Phone: +1 (978) 343-6562

www.sizing.com

### 9.2 Attestation Letter

The auditor's attestation letter is included in the following pages.





Byoung Jun Seo Senior Research Engineer Telecommunications Technology Association (TTA) Bundang-ro 47, Bundang-gu, Seongnam-city Gyeonggi-do, 13591, Republic of Korea

September 5, 2022

I verified the TPC Benchmark<sup>™</sup> C v5.11.0 performance of the following configuration:

Platform:	Supermicro A+ Server 1124US-TNRP
Operating System:	Red Hat Enterprise Linux 8.3
Database Manager:	Goldilocks v3.1 Standard Edition

The results were:

Performance Metric	563 34.5	802 tpmC			
Number of Users	400,000				
Server	Supe	ermicro A+	Server 1124US-TNRP		
CPUs	2x AMD EPYC 7343 (3.2 GHz, 16-core)				
Memory	8,192	2 GB			
Storage	Qty	Size	Туре		
	6	7.68 TB	SATA SSD		
	2	7.68 TB	NVMe SSD		

In my opinion, these performance results were produced in compliance with the TPC requirements for the benchmark.

The following verification items were given special attention:

- · The transactions were correctly implemented
- The database records were the proper size
- · The database was properly scaled and populated
- The ACID properties were met
- · Input data was generated according to the specified percentages
- · The transaction cycle times included the required keying and think times
- The reported response times were correctly measured
- At least 90% of all delivery transactions met the 80 Second completion time limit
- All 90% response times were under the specified maximums

63 Lourdes Dr. | Leominster, MA 01453 | 978-343-6562 | www.sizing.com

- The measurement interval was representative of steady state conditions
- The reported measurement interval was over 120 minutes
- Checkpoint intervals were under 30 minutes
- The 60-day storage requirement was correctly computed
- The system pricing was verified for major components and maintenance

Additional Audit Notes:

None.

Respectfully Yours,

Vour Jahnson

Doug Johnson, Certified TPC Auditor

63 Lourdes Dr. | Leominster, MA 01453 | 978-343-6562 | www.sizing.com

# **Appendix A: Source Code**

The source code and scripts used to implement the benchmark is provided as a soft appendix. This soft appendix includes the following files:

```
\ACID
   \ACID\include
   \ACID\src
   \ACID\include\acid.h
   \ACID\src\atom.c
   \ACID\src\compare.c
   \ACID\src\consist.c
   \ACID\src\Delivery.c
   \ACID\src\isol1.c
   \ACID\src\isol2.c
   \ACID\src\isol3.c
   \ACID\src\isol4.c
   \ACID\src\isol5.c
   \ACID\src\isol6.c
   \ACID\src\isol7.c
   \ACID\src\isol8.c
   \ACID\src\isol9.c
   \ACID\src\Makefile
   \ACID\src\NewOrder.c
   \ACID\src\OrderStatus.c
   \ACID\src\Payment.c
   \ACID\src\support.c
\bin
   \bin\load.sh
\html
   \html\DeliveryInput.html
   \html\MainMenu.html
   \html\NewOrderInput.html
   \html\OrderStatusInput.html
   \html\PaymentInput.html
   \html\StockLevelInput.html
\include
   \include\spt proc.h
   \include\support.h
∖java
   \java\Common.java
   \java\Delivery.java
   \java\NewOrder.java
   \java\OrderStatus.java
   \java\Payment.java
   \java\StockLevel.java
\scripts
   \scripts\analyze system.sql
   \scripts\analyze table.sql
   \scripts\analyze table district.sql
   \scripts\analyze table item.sql
   \scripts\analyze table new order.sql
   \scripts\analyze_table_orders.sql
   \scripts\analyze table order line.sql
```

```
\scripts\analyze table stock.sql
   \scripts\analyze table warehouse.sql
   \scripts\audit.sql
   \scripts\checkpoint.py
   \scripts\count.sql
   \scripts\create_audit_table.sql
   \scripts\create index.sql
   \scripts\create procedure.sql
   \scripts\create table.sql
   \scripts\create tablespace.sql
   \scripts\dbcheck.sql
   \scripts\dbtables.sql
   \scripts\runcheck.sql
   \scripts\sys
         \scripts\sys\be
            \scripts\sys\be\part info.sh
            \scripts\sys\be\reboot info.sh
             \scripts\sys\be\sw info.sh
            \scripts\sys\be\sys info.sh
\src
   \src\free_space.c
   \src\load.c
   \src\load new.c
   \src\Makefile
   \src\support.c
```

# **Appendix B:** Tunable **Parameters**

#### goldilocks.properties.conf

TRANSACTION\_COMMIT\_WRITE\_MODE = 1 TRANSACTION\_TABLE\_SIZE = 1024 UNDO\_RELATION\_COUNT = 1024 UND\_RELATING\_COUNT = 1024 LOG\_BUFFER\_SIZE = 3G LOG\_FILE\_SIZE = 60G LOG\_GROUP\_COUNT = 11 PENDING\_LOG\_BUFFER\_COUNT = 8 SPIN\_COUNT = 1 BUSY\_WAIT\_COUNT = 1000 BUSY\_WAIT\_COUNT = 1000 SYSTEM\_TABLESPACE\_DIR = '/data/db/db1' SYSTEM\_MEMORY\_UNDO\_TABLESPACE\_SIZE = 2G SYSTEM\_MEMORY\_TEMP\_TABLESPACE\_SIZE = 2G SHARED\_MEMORY\_STATIC\_SIZE = 4G PARALLEL\_IO\_GROUP\_1 = '/data/db/db1' PARALLEL\_IO\_GROUP\_2 = '/data/db/db2' PARALLEL\_IO\_GROUP\_3 = '/data/db/db3' PARALLEL\_IO\_GROUP\_5 = '/data/db/db4' PARALLEL\_IO\_GROUP\_5 = '/data/db/db5' LOG\_DIR = '/wal' CLIENT MAX\_COUNT = 1024 CLIENT MAX\_COUNT = 1024 PROCESS MAX\_COUNT = 1024 PARALLEL\_LOAD\_FACTOR = 16 SHARED\_SESSION = NO CONTROL\_FILE\_COUNT = 2 CONTROL\_FILE\_0 = '/wal/control\_0.ctl' CONTROL\_FILE\_1 = '/wal/control\_1.ctl'

#### limit.conf

# /etc/security/limits.conf

" #This file sets the resource limits for the users logged in via PAM. #It does not affect resource limits of the system services.

#Also note that configuration files in /etc/security/limits.d

directory, #which are read in alphabetical order, override the settings in this #file in case the domain is the same or more specific. #That means for example that setting a limit for wildcard domain here #can be overriden with a wildcard setting in a config file in the #subdirectory, but a user specific setting here can be overriden only
#with a user specific setting in the subdirectory.

#Each line describes a limit for a user in the form:

#<domain> <type> <item> <value> #Where: #<domain> can be: - a user name a user name
a group name, with @group syntax
the wildcard \*, for default entry
the wildcard %, can be also used with %group syntax, for maxlogin limit #<type> can have the two values: # - "soft" for enforcing the soft limits # - "hard" for enforcing hard limits #<item> can be one of the following: - core - limits the core file size (KB) - data - max data size (KB) - fsize - maximum filesize (KB) memlock - max locked-in-memory address space (KB) nofile - max number of open file descriptors rss - max resident set size (KB)
stack - max stack size (KB)
cpu - max CPU time (MIN) - nproc - max number of processes
- as - address space limit (KB)
- maxlogins - max number of logins for this user - maxsyslogins - max number of logins on the system - priority - the priority to run user process with

 locks - max number of file locks the user can hold
 sigpending - max number of pending signals
 msgqueue - max memory used by POSIX message queues (bytes)
 nice - max nice priority allowed to raise to values: [-20, " 19] - rtprio - max realtime priority <type> <item> #<domain> <value> soft core hard 10000 rss #@student hard nproc

nproc

#@facul #ftp		hard hard	nproc
#@stude	nt	-	maxlogins
tpcc tpcc tpcc tpcc	soft hard soft hard	nofile nofile nproc nproc	1000000 1000000 unlimited unlimited

soft

#### server.xml

#@faculty

<?xml version='1.0' encoding='utf-8'?>

Licensed to the Apache Software Foundation (ASF) under one or more contributor license agreements. See the NOTICE file distributed with

- this ork for additional information regarding copyright ownership. The ASF licenses this file to You under the Apache License, Version 2 0
- (the "License"); you may not use this file except in compliance with the License. You may obtain a copy of the License at

http://www.apache.org/licenses/LICENSE-2.0

Unless required by applicable law or agreed to in writing, software distributed under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR CONDITIONS OF ANY KIND, either express or implied.

- See the License for the specific language governing permissions and limitations under the License.
- <!-- Note: A "Server" is not itself a "Container", so you may not define subcomponents such as "Valves" at this level. Documentation at /docs/config/server.html
- <Server port="8005" shutdown="SHUTDOWN">

- className="org.apache.catalina.startup.VersionLoggerListener" />
   <!-- Security listener. Documentation at /docs/config/listeners.html
   <Listener className="org.apache.catalina.security.SecurityListener"</pre> /> -->

<!--APR library loader. Documentation at /docs/apr.html -

- <
- Solinging = 0n // solitication of the solution of the sol
- <!-- Prevent memory leaks due to use of particular java/javax APIs-->

<Listener className="org.apache.catalina.core.JreMemoryLeakPreventionListener"

<Listener /> <Listener

className="org.apache.catalina.core.ThreadLocalLeakPreventionListener"

```
<!-- Global JNDI resources
    Documentation at /docs/jndi-resources-howto.html
```

<GlobalNamingResources>

- <!-- Editable user database that can also be used by UserDatabaseRealm to authenticate users
- <Resource name="UserDatabase" auth="Container
- type="org.apache.catalina.UserDatabase" description="User database that can be updated and saved"

</GlobalNamingResources>

```
<!-- A "Service" is a collection of one or more "Connectors" that
share
        a single "Container" Note: A "Service" is not itself a
"Container
                                                                                                   <!-- You should set jvmRoute to support load-balancing via AJP
        so you may not define subcomponents such as "Valves" at this
                                                                                              ie :
level
        Documentation at /docs/config/service.html
  <Service name="Catalina">
     <!--The connectors can use a shared executor, you can define one
or more named thread pools-->
     <Executor name="tomcatThreadPool" namePrefix="catalina-exec-"</pre>
          maxThreads="150" minSpareThreads="4"/>
     -->
     <!-- A "Connector" represents an endpoint by which requests are</p>
received
           and responses are returned. Documentation at
           Java HTTP Connector: /docs/config/http.html (blocking & non-
blocking)
                                                                                              TUNT
           Java AJP Connector: /docs/config/ajp.html
APR (HTTP/AJP) Connector: /docs/apr.html
Define a non-SSL HTTP/1.1 Connector on port 8080
            <Connector port="8080"
                              acceptCount="150000"
maxConnections="141000"
                               connectionTimeout="20000000"
                               maxThreads="1024"
                               maxKeepAliveRequests="-1" keepAliveTimeout="-
1."
protocol="org.apache.coyote.http11.Http11NioProtocol"
 edirectPort="8443"
            />
            <1--
     <Connector port="8080" protocol="HTTP/1.1"
                   connectionTimeout="20000"
                  redirectPort="8443" />
     <!-- A "Connector" using the shared thread pool-->
     <!-- Define a SSL HTTP/1.1 Connector on port 8443
           This connector uses the BIO implementation that requires the
JSSE
           style configuration. When using the APR/native
implementation, the
OpenSSL style configuration is required as described in the
APR/native
           documentation -->
     <1--
     <Connector port="8443"
protocol="org.apache.coyote.http11.Http11Protocol"
maxThreads="150" SSLEnabled="true" scheme="https"
                                                                                              # vendors settings live in /Usr/lib/sysctl.d/.
# To override a whole file, create a new file with the same in
# /etc/sysctl.d/ and put new settings there. To override
# only specific settings, add a file with a lexically later
# name in /etc/sysctl.d/ and put new settings there.
#
secure="true"
                  clientAuth="false" sslProtocol="TLS" />
     <!-- Define an AJP 1.3 Connector on port 8009 -->
<Connector port="8009" protocol="AJP/1.3" redirectPort="8443" />
                                                                                               # For more information, see sysctl.conf(5) and sysctl.d(5).
     <!-- An Engine represents the entry point (within Catalina) that
                                                                                              net.core.netdev max backlog=400000
                                                                                              net.ipv4.tcp_max_syn_backlog=400000
net.ipv4.ip_local_port_range=10000 65535
net.core.somaxconn=65535
processes
           every request. The Engine implementation for Tomcat stand
```

```
alone
        analyzes the HTTP headers included with the request, and
passes them
```

```
<Engine name="Catalina" defaultHost="localhost" jvmRoute="jvm1">
    <Engine name="Catalina" defaultHost="localhost">
       <!--For clustering, please take a look at documentation at:
           /docs/cluster-howto.html (simple how to)
/docs/config/cluster.html (reference documentation) -->
       <Cluster
className="org.apache.catalina.ha.tcp.SimpleTcpCluster"/>
       -->
       <!-- Use the LockOutRealm to prevent attempts to guess user
passwords
            via a brute-force attack -->
       <Realm className="org.apache.catalina.realm.LockOutRealm">
<!-- This Realm uses the UserDatabase configured in the global
               resources under the key "UserDatabase".
                                                            Any edits
               that are performed against this UserDatabase are
immediately
        available for use by the Realm. -->
<Realm className="org.apache.catalina.realm.UserDatabaseRealm"
resourceName="UserDatabase"/>
       </Realm>
       <Host name="localhost" appBase="webapps"
unpackWARs="true" autoDeploy="true">
         <!-- SingleSignOn valve, share authentication between web
applications
              Documentation at: /docs/config/valve.html -->
         <Valve
<!-- Access log processes all example.
               Documentation at: /docs/config/valve.html
              Note: The pattern used is equivalent to using
pattern="common"
        <Valve className="org.apache.catalina.valves.AccessLogValve"
directory="logs"
                s
prefix="localhost_access_log." suffix=".txt"
pattern="%h %l %u %t "%r" %s %b" />-->
       </Host>
    </Engine>
  </Service>
</Server>
Sysctl fe.conf
  sysctl settings are defined through files in
  /usr/lib/sysctl.d/, /run/sysctl.d/, and /etc/sysctl.d/.
  Vendors settings live in /usr/lib/sysctl.d/.
```

net.ipv4.tcp\_tw\_reuse=1

on to the appropriate Host (virtual host). Documentation at /docs/config/engine.html -->

# **Appendix C: Price Quotations**

### DB Server

적일자 :	2022년 6월 13일 월 8	견적서	Que	otation	No :	SW2022-061	UPER* 13-001
<u>수신</u> 참조 아래			11	물시 공항구		사:김성 현 북산티지동열리5차 1107호~1 102-2052-6545   등록관호:1	
	견적금액	일금 이억칠천육백오십사만 원정	₩	27	6,540,000	(VAT포함가)	2
0.	구분	제품명 6.제품사망	수왕	세트	소비차단가	계 와 당 가 (VAT펠 도)	함계금액 (VAT별로)
A.	System	A+ Server 1124US-TNRP	્ય	1		251,400,000	251,400,0
	출랫폼 SKUs	AS -1124US-TNRP					
	Processor	Dual AMD EPYC* 7003/7002 Series Processor, Support CPU TDP up to 280W					C. ma
	Memory	Supports up to 818 Registered ECC DDR4 3200MHz SDRAM, in 32 DIMM slots					-
	Chipset	System on Chip	100			Same Stress	A
	SATA	SATA3 (6Gbps)			1000	Contraction of the	2
	IPMI	Support for Intelligent Platform Management Interface v.2.0					
	Network Controllers	2-port 10GBase-T & 2-port 10G SFP+, based on Intel Carisville X710-AT2					
	PCI-Express	2 PCI-E 4.0 x16 (FH/HL, 9.5°L) 1 PCI-E 4.0 x16 (JP), 1 PCI-E 4.0 x16 (internal proprietary LP slot)	040 //		C	OUH	C. Barrantin D.
	Drive Bays	12 Hot-swap 2.5" U.2 NVMe drive bays					
	Power Supply	(1+1) 1200W Redundant Power Supplies with PMBus, Titanium Level					
	Form Factor	1U Rackmount					
	Warranty	3년 무상, 24시간 On-Site Service					
		Standard Parts List (Items Included)					
	CPU	AMD EPYC <sup>™</sup> 7343 Processor (128M Cache, 3.20GHz, 190W) 16Core (Part Number : PSE-MLN7343-0338)	2				
	Memory	DDR4 256GB ECC REG PC4-25600R	32				
000	NVMe	(Part Number : MEM-DR425L-SL01-LR29) KIOXIA KCD5XLUL7T68 7.68TB NVMe PCIe 4 U.2	2	8			
	SSD	(Parl Number : HDS-TUND-KCD6XLUL7T68) Samsung PM883 7.68TB SATA 6Gb/s	6				
		(Part Number : HDS-S2T1-MZ7LH7T6HMLA05)					
	AOC	AOC-S3908L-H8IR-16DD	1				
	AOC	ConnectX-4 Lx EN network Interface card, 25GbE dual-port SFP28, PCIe3.0 x8 (Part Number : AOC-MCX4121A-ACAT-MLN)	1				
MARK	영업담당자 : 이성을 두	작 010~8028-8688 / swiee@supersolution.co.kr			≜ *	₩	251,400,0
▷ 유효기(	간 : 견책일후, 7일 이내	▷ 결제조건 : 협의			# 21 4	₩	25,140,0
날기조건	: 혐의	▷ 납풍장소 : 귀사 지정장소(서울,경지 지역 외 운임비 별도)			학기금역	₩	276,540,0
친작서 경로	하신 후, 가격 및 모든조건들을 4	·용하는 초건으로 발주 진명을 원하실 경우, 마레 내용을 작성하셔서 주시거나 귀사 발주서를 보내주시기 !	바랍니다.(이	견직서에	날인한 경우, 발주서	와 동일한 흐려운 가입니다)	
1. 발주 담							
2. 탈당자연	락처: 세징보 :				(영관 또	는 직원 날린)	

### WAS Server 1EA

Surrax 4적일7	): 2022년 8월 29일 월3	·····································	Que	otation	No :	8H2022-0	329-001
4	·신: 한국정보통신기술함: ·조: 서행준 선임님 ·라와 같이 건적합니다.		(A	)슈퍼( 8시 코친구	솔루션 대표( 9,9,9,2,244 (가상)		-11082,7072)
E	견적금액	일금 이천일백일십이만 원정	*	21	1,120,000	(VAT포함가)	<u></u>
NO.	<b>守臣</b>	陶瓷箱 医测量外部	수왕	AL	소비자인가	제안단가 (VAT별도)	함게 맘액 (VAT별도)
A	System	A+ Server 1114S-WN10RT	4	1		19,200,000	19,200,00
	불帙書 SKUs	AS -1114S-WN10RT					
	Processor	Single AMD EPYC™ 7003/7002 Series Processor, Support CPU TDP up to 280W				6	
	Memory	Up to 418 3DS ECC DDR4-3200MHz RDIMM/LRDIMM, in 16 DIMM slats	-				
	Chipset	System on Chip	1			Alexander	
	SATA	SATA3 (6Gbps)	1		/		- and a second
	IPMI	Support for Intelligent Platform Management Interface v.2.0	•		200		
	Network Controllers	Dual 10GBase-T LAN with via Broadcom BCM57416	ः				
		2 PCI-E 4.0 x16 (FHHL) slots			1		
	PCI-Express	1 PCI-E 4.0 x16 (LP) slot, 2 PCI-E 4.0 x2 NVMe M2 slots	-		Sec. States		A TIME CONTRACTOR
	Drive Bays	10 Hot-swap U.2 NVMe4/NVMe3/SATA3 fully hybrid drive support					
	Power Supply	(1+1) 750W Redundant Platinum Power Supplies with PMBus					
	Form Factor	1U Rackmount					
	Warranty	3년 무상, 24시간 On-Site Service					
		Standard Parts List (Items Included)					
1	CPU	AMD EPYC <sup>™</sup> 7713 Processor (256M Cache, 2.0GHz, 225W) 64Core (Part Number : PSE-MLN7713-0344)	1				
[	Memory	DDR4 16GB ECC REG PC4-25600R (Part Number : MEM-DR416L-CL06-ER32)	8	1			
Ē	M.2 NVMe	Intel DC P4511, 1TB NVMe PCI-E 3.1 x4, M.2 (Part Number : HDS-INND-SSDPELKX010T8)	1	1			
1	AOC	ConnectX-5 Ex EN network Interface card, 100GbE dual-port QSFP28, PCIe4.0 x16 (Part Number: AOC-MCX516A-CDAT)	1				
1	AOC	ConnectX-4 Lx EN network Interface card, 25GbE dual-port SFP28, PCIe3.0 x8	1				
EMARK	영영당단자 : 관사형 6	(Part Number : AOC-MCX4121A-ACAT-MLN) [편] 010-6807-8683 / shkang@supersolution.co.kr	1		* 4	#	19,200,00
	기간 : 견적일후, 7일 이내	▷ 펼쳐조건 : 혐의				₩	1,920,00
					A LANGER DATE	(16)72 (17)4(1)	10000
> 납기조	전화 전화 이 이 이 이 이 이 이 이 이 이 이 이 이 이 이 이 이	> 납품장소 : 귀사 지정장소(서울,경지 지역 외 운영비 별도)			함계금역	#	21,120,00
1. 발주 2. 영양 3. 배송 <sup>3</sup>	담당자:	·용하는 조건으로 발주 진영을 원하실 경우, 이래 내용을 작성하셔서 주시거나 귀사 발주서를 보내주시기 바랍니다.(	1 15 M	4 202		발한 포력을 가입니다) 또는 직안 날인)	

### WAS Server 2EA

2022년 8월 31일 수요	2일	Que	tation	No‡	SH2022-083	1-001
서병준 선입님		44	이 공항적	7 安美里 244 (74位)	R, 백산리지털별리6차 1307호~1	
견적금액	일금 사천이백이십사만 원정	₩	4	2,240,000	(VAT포함가)	
구름	개풍명 & 개풍사망	98	세트	소비차당가	제안단7F (VAT펠도)	함계금액 (VAT별도)
System	A+ Server 1114S-WN10RT	4	2		19,200,000	38,400,00
番ț 甚 9KUs	AS -1114S-WN10RT					
Processor	Single AMD EPYC <sup>W</sup> 7003/7002 Series Processor, Support CPU TDP up to 280W				A atroly	Carolina Stationers
Memory	Up to 4TB 3DS ECC DDR4-3200MHz RDIMM/LRDIMM, in 16 DIMM slots	1			A.	
Chipset	System on Chip	1		1		-
SATA	SATA3 (6Gbps)	1			-	and a later of the second
IPMI	Support for Intelligent Platform Management Interface v2.0			(Aller		
Network Controllers	Dual 10GBase-T LAN with via Broadcom BCM57416					
PCI-Express	2 PCI-E 4.0 x16 (PHHL) slots 1 PCI-E 4.0 x16 (IP) slot, 2 PCI-E 4.0 x2 NVMe M.2 slots	1				
Drive Bays	10 Hot-swap U.2 NVMe4/NVMe3/SATA3 fully hybrid drive support					
Power Supply	(1+1) 750W Redundant Platinum Power Supplies with PMBus					
Form Factor	1U Rackmount	1				
Warranty	3년 무상, 24시간 On-Site Service					
17	Standard Parts List (Items Included)	-12 - 12 				
CPU	AMD EPYC™ 7713 Processor (256M Cache, 2.0GHz, 225W) 64Core (Part Number : PSE-MLN7713-0344)	1				
Memory	DDR4 15GB ECC REG PC4-25500R	8				
U.2 NVMe	Samsung PM9A3, 1.92TB NVMe U.2 2.5" PCIe 4.0 x4	2				
AOC	ConnectX-5 Ex EN network interface card, 100GbE dual-port QSFP28, PCIe4.0 x16	1				
영업담당자 : 감신혁 대				<b>≙</b> ₹	₩	38,400,00
: 견적일후, 7일 이내	▷ 결제조건 : 협의			부가세	₩	3,840,00
범의	> 납품장소 : 귀사 지정장소(서울 경지 지역 외 운임비 별도)			학계금역	₩	42,240,00
	한국정보통신기 술험적 서병준 선일님 사람준 선일님 구문 <b>System</b> 물랫폼 3KUs Processor Memory Chipset SATA IPMI Network Controllers PCI-Express Drive Bays Power Supply Eorm Factor Warranty U.2 NVMe AOC	세명은 선명님         박 같이 견적회나다.         전력규액       입금 사천이백이십사만 원정         무응       제품명 & 제품사망         System       A+ Server 11145-WN10RT         출밧용 9KUs       AS -11145-WN10RT         출밧용 9KUs       AS -11145-WN10RT         Processor       Single AMD EPYC <sup>IM</sup> 7003/7002 Series Processor, Support CPU TDP up to 280W         Memory       Up to 4TB 3DS ECC DDR4-3200MHz RDIMM/LRDIMM, in 16 DIMM slots         Chipset       System on Chip         SATA       SATA3 (6Gbps)         IPMI       Support for Intelligent Platform Management Interface v.2.0         Network Controllers       Dual 10GBase-T LAN with via Broadcom BCM57416         PCI-Express       2 PCI-E 4.0 x16 (PMHL) slots         1 PCI-E 4.0 x16 (FMHL) slots       1 PCI-E 4.0 x16 (FMHL) slots         Power Supply       (1+1) 750W Redundant Platinum Power Supplies with PMBus         Form Factor       1U Rackmount         Warranty       St P 4: 4.41(1) On-Site Service         Standard Parts List (Items Included)         CPU       AMD EPPC <sup>IM</sup> 7713 Processor (256M Cache, 2.0GHz, 225W) 64Core (Part Number: 1PSE-MUN711-0544)         Memory       DDR4 16GB ECC REG PC4-25600R (Parts 2:57 PCie 4.0 x4         V.2 NVMe       Samuerg PM9A3, 1.52TB NUM VIE U.2.2.57 PCie 4.0 x4 <td>한국정보류십기술법칙 서범은 섬임님         (주           관점 여         입금 사천이백이십사만 원정           전점 여         입금 사천이백이십사만 원정           전점 여         입금 사천이백이십사만 원정           무분         제품범 &amp; 제품차 원           양명         System           A+ Server 1114S-WN10RT         4           물릿분 3KUs         AS -1114S-WN10RT         4           물릿분 3KUs         AS -1114S-WN10RT         4           Processor         Single AMD EPYC* 7003/002 Series Processor, Support CPU TDP up to 280W         4           Memory         Up to 418 3DS ECC DDR4-3200MHz RDIMM/LRDIMM, in 16 DIMM slots.         1           Chippet         System on Chip         5           SATA         SATA (46Gbps)         1           IPM         Support for Intelligent Platform Management Interface v.2.0         1           Network Controllier:         Dual 10GBase-TLAN with via Broadcom BCM57416         1           PCI-Express         2 PCI-E 4.0 x16 6/PHIQ slots         1           PCI-Express         1 PCI-E 4.0 x16 6/P Slot, 2 PCI-E 4.0 x2 NVMe M2 slots         1           Power Supply         (1+1) 750W Redundant Platinum Power Supples with PMBus         1           Power Supply         (1+1) 750W Redundant Platinum Power Supples with PMBus         1           Form Facter         10 RacK</td> <td>한국정보통신기술합복         연위수퍼           전쟁금액         입금 사천이백이십사만 원정         분 20           전쟁금액         입금 사천이백이십사만 원정         분 4           전쟁금액         입금 사천이백이십사만 원정         분 4           전통 10         At Server 1114S-WN10RT         역 2           환경 14         5ystem         At Server 1114S-WN10RT         역 2           환경 15         AS -1114S-WN10RT         역 2         2           환경 15         AS -1114S-WN10RT         역 2         2           Processor         Single AMD EPVC** 7003/7002 Series Processor, Support CPU TDP up to 200W         4           Memory         Up to 41B 3DS ECC DDR4-3200MH:R RDIMM(IRDIMM, in 16 DIMM slots         1           Chipset         System on Chip         5         1           SATA         SATA3 (660ps)         1         1           IPM         Support for Intelligent Platform Management Interface v2.0         1         1           Network Controllers         Dual 10GBase-T LAN with via Broadcom BCM57416         1         1           PCI-E 40 x16 (PH1t) slots         1 PCI-E 40 x16 (PH1t) slots         1         1           PCI-E 40 x16 (PH1t) slots         1 PCI-E 40 x16 (PH1t) slots         1         1           PCI-E 40 x16 (PH1t) slots         1 PCI-E 40 x16 (PH1t) slots         &lt;</td> <td>환급성 보육 신기 술 환력         Rh슈퍼 솔 루션 대표           전력 관객         일급 사천이백이십사만 원정         *** 시뢰구 동포 1×4 (rote)           전력 관객         일급 사천이백이십사만 원정         *** 42,240,000           구분         제품변 &amp; 제품번 * 제품번 * 제품번 *         ***           경력 관객         일급 사천이백이십사만 원정         *** 42,240,000           구분         제품변 * 제품번 * 제품번 *         ***           경험 관         수당 제료         소비차원가           System         A+ Server 1114S-WN10RT         ·**         ·**           Processor         Single AMD EPYC** 7003/7002 Series Processor, Support CPU TDP up to 2200W         ·**         ·**           Processor         Single AMD EPYC** 7003/7002 Series Processor, Support CPU TDP up to 2200W         ·**         ·**           Processor         Single AMD EPYC** 7003/7002 Series Processor, Support CPU TDP up to 2200W         ·**         ·**           Memory         Up to 418 3D5 ECC DDR4-3200MH: RDIMMURDDMM, in 16 DIMM slots         ·**         ·**           Chipset         System on Chip         ·**         ·**         ·**           SATA         SATA (20Gepti         1         1         ·**           IPMI         Support for Intelligent Platform Management Interface v2.0         1         1           Network Controller         Dual 1068aer 1 LN with va Broadcom BCM57416<td>환전성보육신기술함集 개별은 선정님         편의 대부가 2011           관련 전성님         편의 기관 2011           전원 비수료:         전원 비수료:           전 원 비수료:         전원 비수료:           전 원 비수료:         전원 비수료:           정당함m         A+ Server 11145-WN10RT           용 전 용 11145-WN10RT         석           Processo:         Single AMD EPIC* 7002/7002 Series Processo: Support CPU TDP up to 280W           Memory         Up to 418 305 ECC DDR4-300M+tr RDIAMULRDIMM, in 16 DIMM slots.           Chipaet         Spitem on Chip           SATA         SATA 316(Goppi           IPM         Support for Intelligent Platform Management Interface v2.0           Network:         Critical 16 Pi slot; 2 PIC # 40 x8 / 24 / 30 / 5 PH is dex           IPM         Support 6 for Intelligent Platform Management Interface v2.0           Network:         Critical 16 Pi slot; 2 PIC # 40 x8 / 24 / 30 / 5 PH is dex           Power Supply         11 Pice 40 x16 / 5 PH iside:</td></td>	한국정보류십기술법칙 서범은 섬임님         (주           관점 여         입금 사천이백이십사만 원정           전점 여         입금 사천이백이십사만 원정           전점 여         입금 사천이백이십사만 원정           무분         제품범 & 제품차 원           양명         System           A+ Server 1114S-WN10RT         4           물릿분 3KUs         AS -1114S-WN10RT         4           물릿분 3KUs         AS -1114S-WN10RT         4           Processor         Single AMD EPYC* 7003/002 Series Processor, Support CPU TDP up to 280W         4           Memory         Up to 418 3DS ECC DDR4-3200MHz RDIMM/LRDIMM, in 16 DIMM slots.         1           Chippet         System on Chip         5           SATA         SATA (46Gbps)         1           IPM         Support for Intelligent Platform Management Interface v.2.0         1           Network Controllier:         Dual 10GBase-TLAN with via Broadcom BCM57416         1           PCI-Express         2 PCI-E 4.0 x16 6/PHIQ slots         1           PCI-Express         1 PCI-E 4.0 x16 6/P Slot, 2 PCI-E 4.0 x2 NVMe M2 slots         1           Power Supply         (1+1) 750W Redundant Platinum Power Supples with PMBus         1           Power Supply         (1+1) 750W Redundant Platinum Power Supples with PMBus         1           Form Facter         10 RacK	한국정보통신기술합복         연위수퍼           전쟁금액         입금 사천이백이십사만 원정         분 20           전쟁금액         입금 사천이백이십사만 원정         분 4           전쟁금액         입금 사천이백이십사만 원정         분 4           전통 10         At Server 1114S-WN10RT         역 2           환경 14         5ystem         At Server 1114S-WN10RT         역 2           환경 15         AS -1114S-WN10RT         역 2         2           환경 15         AS -1114S-WN10RT         역 2         2           Processor         Single AMD EPVC** 7003/7002 Series Processor, Support CPU TDP up to 200W         4           Memory         Up to 41B 3DS ECC DDR4-3200MH:R RDIMM(IRDIMM, in 16 DIMM slots         1           Chipset         System on Chip         5         1           SATA         SATA3 (660ps)         1         1           IPM         Support for Intelligent Platform Management Interface v2.0         1         1           Network Controllers         Dual 10GBase-T LAN with via Broadcom BCM57416         1         1           PCI-E 40 x16 (PH1t) slots         1 PCI-E 40 x16 (PH1t) slots         1         1           PCI-E 40 x16 (PH1t) slots         1 PCI-E 40 x16 (PH1t) slots         1         1           PCI-E 40 x16 (PH1t) slots         1 PCI-E 40 x16 (PH1t) slots         <	환급성 보육 신기 술 환력         Rh슈퍼 솔 루션 대표           전력 관객         일급 사천이백이십사만 원정         *** 시뢰구 동포 1×4 (rote)           전력 관객         일급 사천이백이십사만 원정         *** 42,240,000           구분         제품변 & 제품번 * 제품번 * 제품번 *         ***           경력 관객         일급 사천이백이십사만 원정         *** 42,240,000           구분         제품변 * 제품번 * 제품번 *         ***           경험 관         수당 제료         소비차원가           System         A+ Server 1114S-WN10RT         ·**         ·**           Processor         Single AMD EPYC** 7003/7002 Series Processor, Support CPU TDP up to 2200W         ·**         ·**           Processor         Single AMD EPYC** 7003/7002 Series Processor, Support CPU TDP up to 2200W         ·**         ·**           Processor         Single AMD EPYC** 7003/7002 Series Processor, Support CPU TDP up to 2200W         ·**         ·**           Memory         Up to 418 3D5 ECC DDR4-3200MH: RDIMMURDDMM, in 16 DIMM slots         ·**         ·**           Chipset         System on Chip         ·**         ·**         ·**           SATA         SATA (20Gepti         1         1         ·**           IPMI         Support for Intelligent Platform Management Interface v2.0         1         1           Network Controller         Dual 1068aer 1 LN with va Broadcom BCM57416 <td>환전성보육신기술함集 개별은 선정님         편의 대부가 2011           관련 전성님         편의 기관 2011           전원 비수료:         전원 비수료:           전 원 비수료:         전원 비수료:           전 원 비수료:         전원 비수료:           정당함m         A+ Server 11145-WN10RT           용 전 용 11145-WN10RT         석           Processo:         Single AMD EPIC* 7002/7002 Series Processo: Support CPU TDP up to 280W           Memory         Up to 418 305 ECC DDR4-300M+tr RDIAMULRDIMM, in 16 DIMM slots.           Chipaet         Spitem on Chip           SATA         SATA 316(Goppi           IPM         Support for Intelligent Platform Management Interface v2.0           Network:         Critical 16 Pi slot; 2 PIC # 40 x8 / 24 / 30 / 5 PH is dex           IPM         Support 6 for Intelligent Platform Management Interface v2.0           Network:         Critical 16 Pi slot; 2 PIC # 40 x8 / 24 / 30 / 5 PH is dex           Power Supply         11 Pice 40 x16 / 5 PH iside:</td>	환전성보육신기술함集 개별은 선정님         편의 대부가 2011           관련 전성님         편의 기관 2011           전원 비수료:         전원 비수료:           전 원 비수료:         전원 비수료:           전 원 비수료:         전원 비수료:           정당함m         A+ Server 11145-WN10RT           용 전 용 11145-WN10RT         석           Processo:         Single AMD EPIC* 7002/7002 Series Processo: Support CPU TDP up to 280W           Memory         Up to 418 305 ECC DDR4-300M+tr RDIAMULRDIMM, in 16 DIMM slots.           Chipaet         Spitem on Chip           SATA         SATA 316(Goppi           IPM         Support for Intelligent Platform Management Interface v2.0           Network:         Critical 16 Pi slot; 2 PIC # 40 x8 / 24 / 30 / 5 PH is dex           IPM         Support 6 for Intelligent Platform Management Interface v2.0           Network:         Critical 16 Pi slot; 2 PIC # 40 x8 / 24 / 30 / 5 PH is dex           Power Supply         11 Pice 40 x16 / 5 PH iside:

### RHEL/JWS



(유락플레이스 135-120 서울시 강남구 신사동 634-10 옾당빌딩 3층 Tel.02)6251.7788 Fax.02)6251.6677

71

rockPLACE, Inc.

3F, Yundang bldg, 634-10, Shinsa-dong, Gangnam-gu,Seoul, Korea Tel : 822-6251-7788 Fax: 822-6251-6677

저서

REF No.	: 2022RP08-2503	TERMS AND CONDITION
DATE	: 2022. 08. 25.	
COMPANY	: TTA	납 기 : 발주후 4주이내
ATTN	: 서 병 준 선임 연구원님 귀하 TEL : 010-5110-5598	유지보수 :계약후1년
Email	: sbj8388@tta.or.kr	결제조건 : 의월말 현금
FROM	: ㈜ 락플레이스 정 경환 차장 TEL : 010-4298-3447	유효기간 : 견적일로부터 1개월

### 下記와 같이 見積합니다.

## ㈜ 락플레이스

대표이사 서 동 식

Part No.	Description	수량	소비자가	공급단가	공급합계
OS	Red Hat Enterprise Linux Operating System Platform				
RH00004	Red Hat Enterprise Linux Server, Standard (Physical or Virtual Nodes) 3Year support : Easy ISOs: OS, Source, Documentation ISO Images 가상화 Guest OS : 2guests Red Hat Network 서비스 : 3Year Phone,email Support : 09:00 ~ 17:00 Scope of Coverage : Standard Maximum Memory Support: Unlimited	4	4,098,000	2,540,000	10,160,000
OS 기술지원	현장 방문 지원서비스 - 옵션	e de	9		
P-CSP(OS)F3	rockPLACE Support Carepack - OS Maintenance - per server (3년) 이메일, 전화, 원격지원, 현장지원 서비스 3Year 24x7, 4hr response On Site Support - 연간 10회 방문지원 - Installation & Startup Service Included - Problem tracking/Emergency assistance - 방문 점검 : 분기점검 (선택) 점검 보고서 전달 - Update, Patch 작업 지원 - 서비스, 시스템 환경 설정 변경 지원 - 서비스, 시스템 환경 설정 변경 지원 - 엔수 시험, 성능 시험, 비상 복구 훈련 지원 - 메이저 버전 업그레이드 및 마이그레이션의 경우 본 기술지원에 포함되지 않습니다. (법도협의 필요)	4	6,000,000	3,000,000	12,000,000
	소 계 금 액				22,160,000

Part No.	Description	수량	소비자가	공급단가	공급합계
WEB	Red Hat JBoss Web Server			44	
MW0290056F3	Red Hat JBoss Web Server, 64-Core Standard 3Year - 전화/웹 지원 : 월-금, 9 a.m 5 p.m. 4시간내 응답 - unlimited incidents, - 4 business hour SLA	3	28,005,000	17,923,200	53,769,60
연간기술지원	현장 방문 지원서비스 - <mark>옵션</mark>				
RP-CSP(WAS)F3	rockPLACE Support Carepack - JBoss Stadnard (3년) Per 64Core 3 Year, 24x7, 4hr response 월격 & 현장 방문 지원 : 연간 10회 - Installation & Startup Service Included - 환경 설정 및 구성 지원 - Problem tracking/Emergency assistance - Update, Patch 작업 지원 - 서비스, 시스템 환경 변경 지원 - 서비스, 시스템 환경 변경 지원 - 인수 시험, 성능 시험, 비상 북구 훈련 지원 - 메이저 버전 업그레이드 및 마이그레이션의 경우 본 기술지원에 포함되지 않습니다. (별도협의 필요)	3	30,000,000	15,000,000	45,000,00
	소 계 금 액				98,769,60

합 계	120,929,600
부가세	12,092,960
합 계(부가세포함)	133,022,560

Remarks	
1. 커뮤니티 제품의 경우	L1,L2 레벨의 기술지원만 가능하며, L3(벤더지원:코어분석, 신종버그패치, 업그레이드등) 레벨의 기술지원은 제약사항이 발생됩니다.
2. 레드햇 서브스크립션은	은 구독형 제품으로 서비스 기간 만료시 갱신하셔야 합니다.
3. 발주 시에는 반드시 고	!객정보(엔드유져명, 담당자, 연락처, Email)가 있어야 합니다.
4. 원격/현장지원 등 기술	지원이 필요하실 경우에는 케어팩을 구매하셔야 합니다.

### Network

<b>iCRAFT</b>	아이크래프트 섞			www.icraft21.com			
	06210, 서울특별시 강남구 테헤란로 44길 8 (역삼동), <mark>아</mark> 이콘 빌딩 8충	Tel. 02-541-0474(대표)	Fax. 02-543-1442				
	7	ᅧ <u>전</u> 서					
	Ĺ.						
수 신 TTA				220-81-75308			
사 업 명 TPC 공인 시험 성적 성능 테스트용 스위치 견적			아이크래프트㈜				
견적일 2022년 8월 31일				박우진 (직인생략)			

담당자 서병준 연구원님

아이크래프트웨 박 우 진 (직인생략) 06210, 서울특별시 강남구 테헤란로 44길 8 (역삼동), 아이콘빌딩 8F 소프트웨어 개발, 컨설팅 통신장비 외

[단위: 원, VAT별도]

#### 견적금액: ₩

48,388,000 (VAT별도)

No.	0000		수량	-	공급가		
	모델명	격품상체		탄위	공급단가	공급금액	비고
NVIDIA Netw	vorking Solution - Mella	inox					
1	MSN2700-CS2F	Mellanox Spectrum based 100GbE 1U Open Ethernet Switch with Onyx, 32 QSFP28 ports, 2 Power Supplies (AC), x86 CPU, standard depth, P2C airflow, Rail Kit		EA	36,844,000	36,844,000	워런티 1년 포함
2	유지보수	장애지원, 12개월, 24x7, 4시간 이내 접수		년	3,770,000	11,310,000	
3	MCP7F00-A001R30N	N Mellanox passive copper hybrid cable, ETH 100GbE to 4x25GbE, QSFP28 to 4x5FP28, 1m, Colored, 30AWG, CA-N		EA	234,000	234,000	
		합 계 (VAT별도)			₩	48,388,000	

작성자. DC사업본부 영업팀 이정호 부장 Mobile, 010-8899-0913 Email, jhlee@icraft21,com 📀 DVIDIA,

#### \* Remark

- 1 본 견적은 TPC 공인 시험 성적 성능 테스트용 스위치 견적 사업에 한합니다
- 2 결제조건 : 납품 후 현금결제

3 유효기간 : 견적 후 4개월 이내

- 4 납품일정 : 글로벌 칩셋 수급 이슈로 납품일정이 변동적입니다 (제조사 공지 기준: 26-32주 혹은 이상)
- 5 설치지원 : 별도 (1인 1일 기준, 서울/경기: 50만원, 지방: 70만원, 케이블링 작업 비용 별도)

6 발주 전 스위치 Airflow 를 확인해 주세요 (발주 후 변경 볼가)

### DBMS

견 적 서

### TTA 貴中

Title : TPC-C Performance&Quality Authentication

■ 수 신 : 서병준 선임연구원님 (010-5110-5598, sbj8388@tta.or.kr)

- 견적일자 : 2022년 08월 25일
- 유효기간 : 견적일로부터 4개월



※ Goldilocks Standard Edition for LINUX 1식						(단위 : 원	
No.	Description	Unit Price	Q'ty	Total Price	Offer Price	비고	
[320	Core]						
1	Goldilocks Ver 3.1 DBMS Standard Edition	64,000,000	1 Set(s)	64,000,000	22,400,000		
	- Query Processes Module		Ĵ	2			
	- Storage Management Module	0					
	소 계 (부가세 별도)			64,000,000	22,400,000		
2	DBMS Implementaion & Supports	20,000,000	3 Set(s)	60,000,000	9,600,000		
	소 계 (부가세 별도)	9,600,000					
	합 계 (부가세 별도)	32,000,000					
_							
	총 합 계 (부가서	32,000,000					

\* Remarks

- For Technical supports, it indicates 24 x 7 x 4 hours of support

### Keyboard/Monitor

